

Ellsworth
Arroyo3 PVT Schematics

PCB Version : 6050A2516401 XTAL LIST

Table with 2 columns: CONTENTS, SHEET. Rows include: 01: TABLE OF CONTENTS, 02: SYSTEM BLOCK DIAGRAM, 03: CLK/SMBUS/RESET MAP, 04: AMD FT1 (1/2), 05: AMD FT1 (2/2), 06: DDR3 SODIMM A-1 CHANNEL, 07: DDR3 SODIMM A-2 CHANNEL, 08: SCALAR ANX3110, 09: AMD HUDSON D3L (1/4), 10: AMD HUDSON D3L (2/4), 11: AMD HUDSON D3L (3/4), 12: AMD HUDSON D3L (4/4), 13: AUDIO ALC269-VC, 14: LAN RTL8175E-CG/LED, 15: CR/HDD/ODD/WEBCAM/D-MIC, 16: USB/MINI PCI-E/FAN, 17: USB3.0 CONN, 18: EC IT8519E, 19: DEBUG CIRCUIT, 20: SYSTEM +3V/+5V, 21: +VDDIO_MEM/MEM_VTT/DC-IN, 22: +1.8V/+12V/+1.2V/+1.1V/DIS, 23: +1.1V_S3/+1.05V, 24: VDDCR_CPU/VDDCR_NB, 25: STACK UP 6L, 26: SCREW HOLE/SILK SCREEN, 27: Power Sequence Diagram, 28: POWER SEQUENCE, 29: GPIO TABLE, 30: HISTORY

Table with 6 columns: PARTS, DIP/SMT, Frequency, PPM, CL. Rows include: 1 X2 SMT 25MHz ±20 12pF/15pF, 2 X3 SMT NC/25MHz ±20 10pF/10pF, 3 X4 SMT 32.768KHz ±20 10pF/10pF, 4 X5 SMT 25MHz ±20 12pF/12pF, 5 X6 SMT NC/32.768KHz ±20 12pF/12pF

USB LIST

Table with 3 columns: PORT, NET NAME, FUNCTION. Rows include: USB_HSD0P/N, USB0_DEBUG_P/N, REAR IO; USB_HSD1P/N, USB1P/N, REAR IO; USB_HSD2P/N, USB2P/N, REAR IO; USB_HSD3P/N, USB3P/N, REAR IO; USB_HSD5P/N, USB5_WLAN_P/N, WLAN/BT; USB_HSD8P/N, USB8_WEBCAM_P/N, WEBCAM; USB_SS_TX0P/N, USB3.0_TX0P/N, SIDE IO; USB_SS_TX1P/N, USB3.0_TX1P/N, SIDE IO

SATA LIST

Table with 3 columns: PORT, NET NAME, FUNCTION. Rows include: SATA_TX0P/N, SATA0_TXP/N, SATA HDD; SATA_RX0P/N, SATA0_RXP/N, SATA HDD; SATA_TX1P/N, SATA1_TXP/N, SATA ODD; SATA_RX1P/N, SATA1_RXP/N, SATA ODD

PCI-E LIST

Table with 3 columns: PORT, NET NAME, FUNCTION. Rows include: GPP_TX3P/N, PCIE_WLAN_TXP/N, WIRELESS LAN; GPP_RX3P/N, PCIE_WLAN_RXP/N, WIRELESS LAN; GPP_TX1P/N, PCIE_LAN_TXP/N, LAN; GPP_RX1P/N, PCIE_LAN_RXP/N, LAN; GPP_TX2P/N, PCIE_CARD_TXP/N, CARDREADER; GPP_RX2P/N, PCIE_CARD_RXP/N, CARDREADER

POWER STATES

Table with 8 columns: STATE, SIGNAL, VOTAGE, S0, S3, S5, S5 WARM, G3, REMARK. Rows include: FCH_SLP_S3#, FCH_SLP_S5#, S5_PWR_ON, +VBAT_IN, +VIN, +3V_ALW/+5V_ALW, +3V_S5, +1.1V_S5, +5V_S3, +3V_S3, +VDDIO_MEM, +1.1V_S3, +MEM_VTT, +3V_S0/+5V_S0, +1.8V_S0, +12V_S0, +1.1V_S0, +1.5V_S0, +1.2V_S0, +1.05V_S0, +VDDCR_CPU, +VDDCR_NB

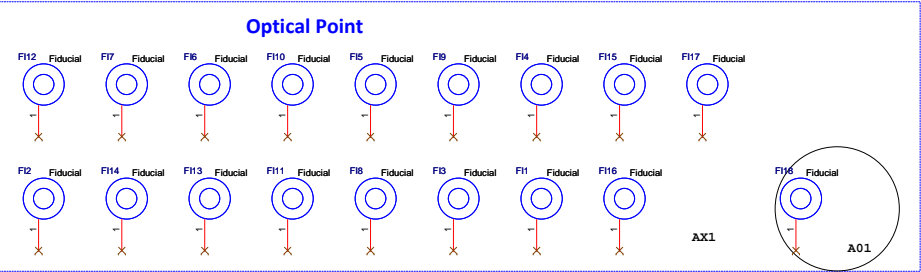
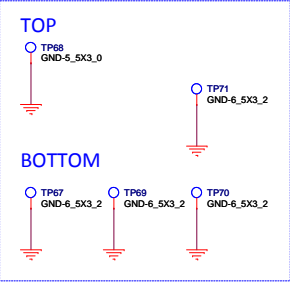
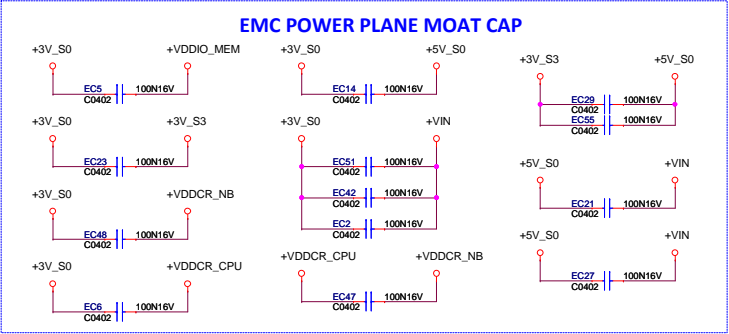
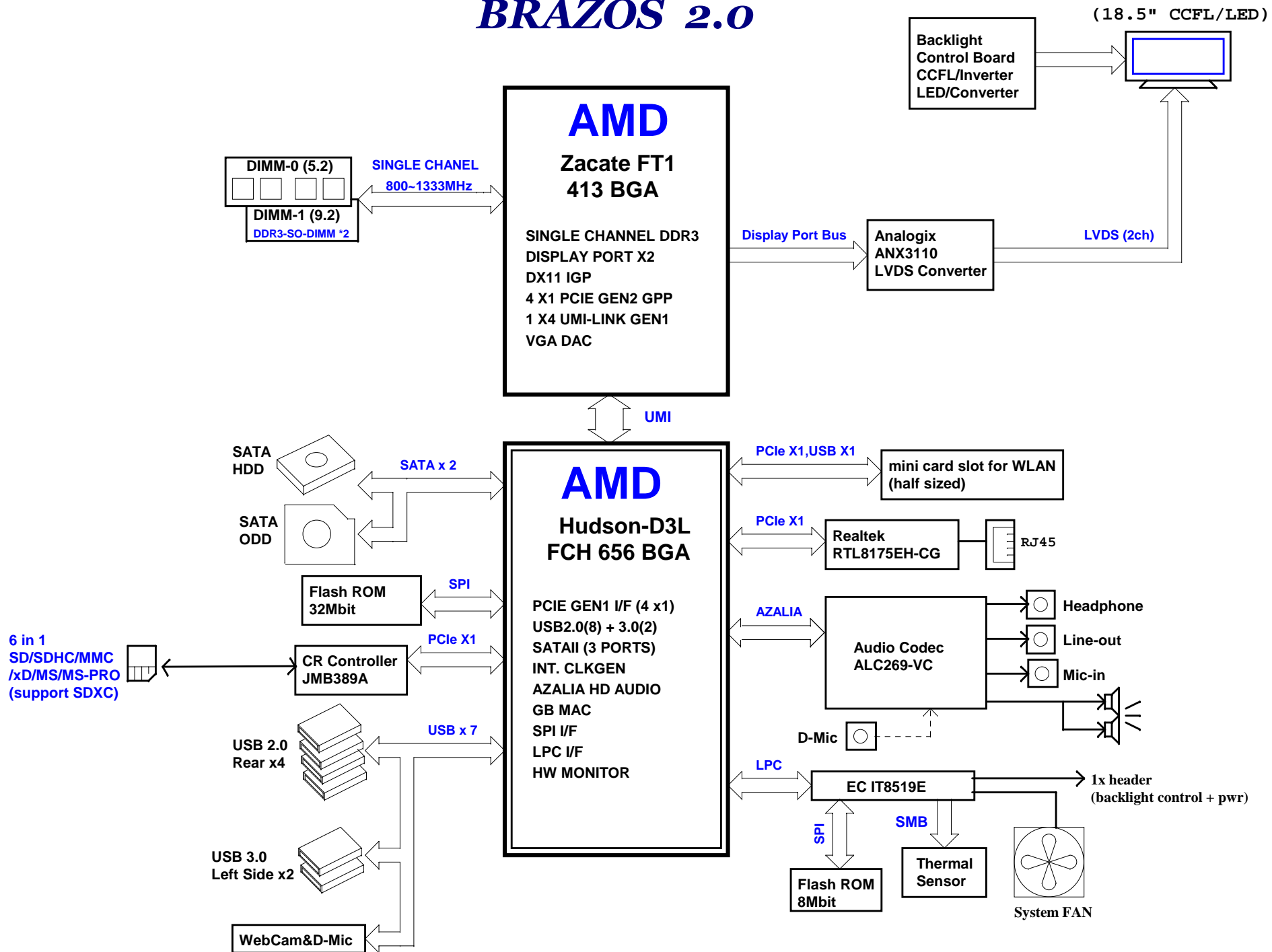


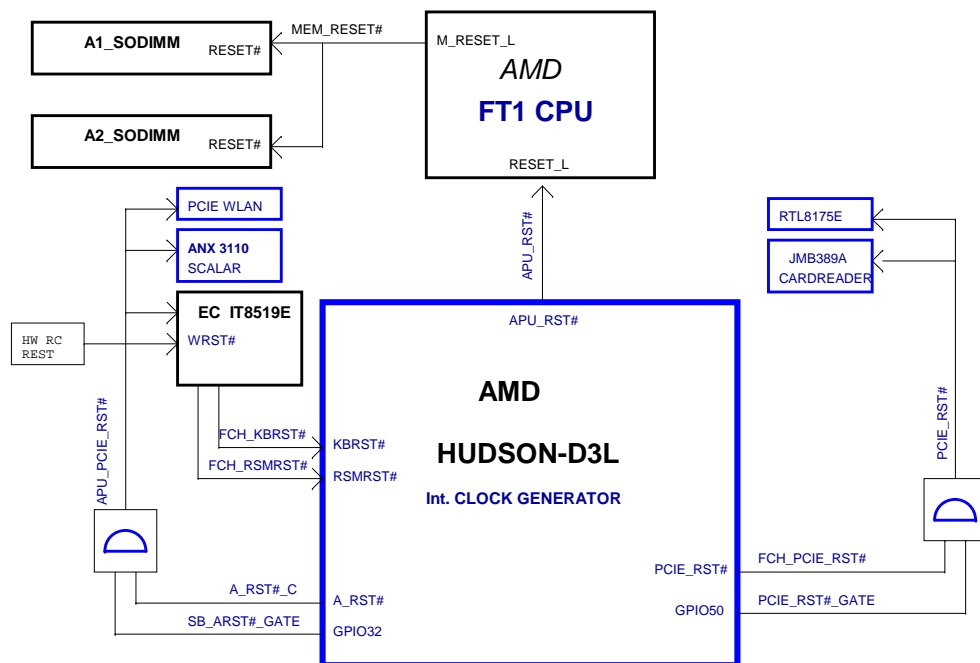
Table with 4 columns: TPV (Top Victory Electronics Co., Ltd.), OEM MODEL, HP_Arroyo3, Size C. Rows include: 1, Arroyo3, 6050A2516401, 1 of 30

BRAZOS 2.0

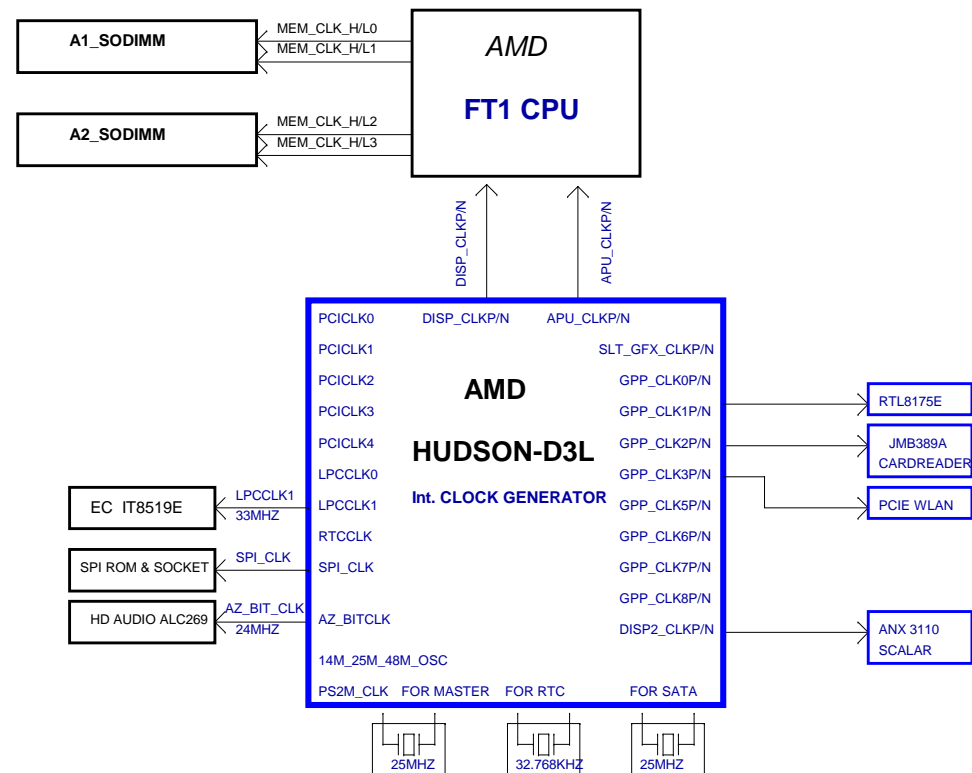


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_ArroyoS	Size	C
TPV MODEL	ArroyoS	Rev	AG2	
PCB NAME	0050A2516401	Sheet	2	of 30
Date	Tuesday, May 22, 2012			

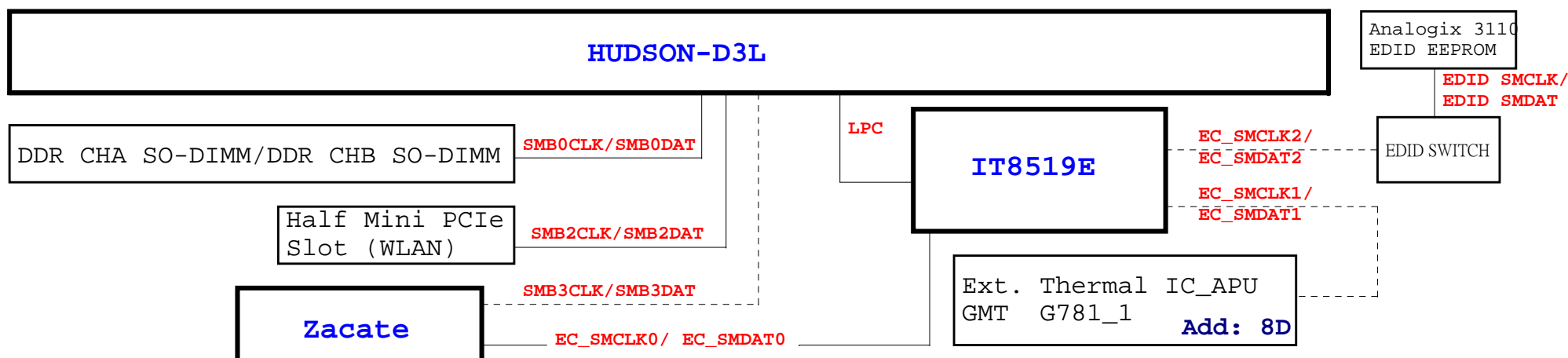
RESET Block Diagram



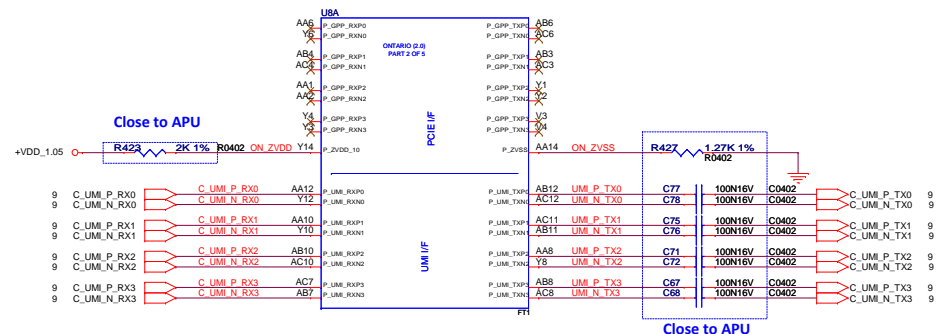
INTERNAL CLOCK MODE



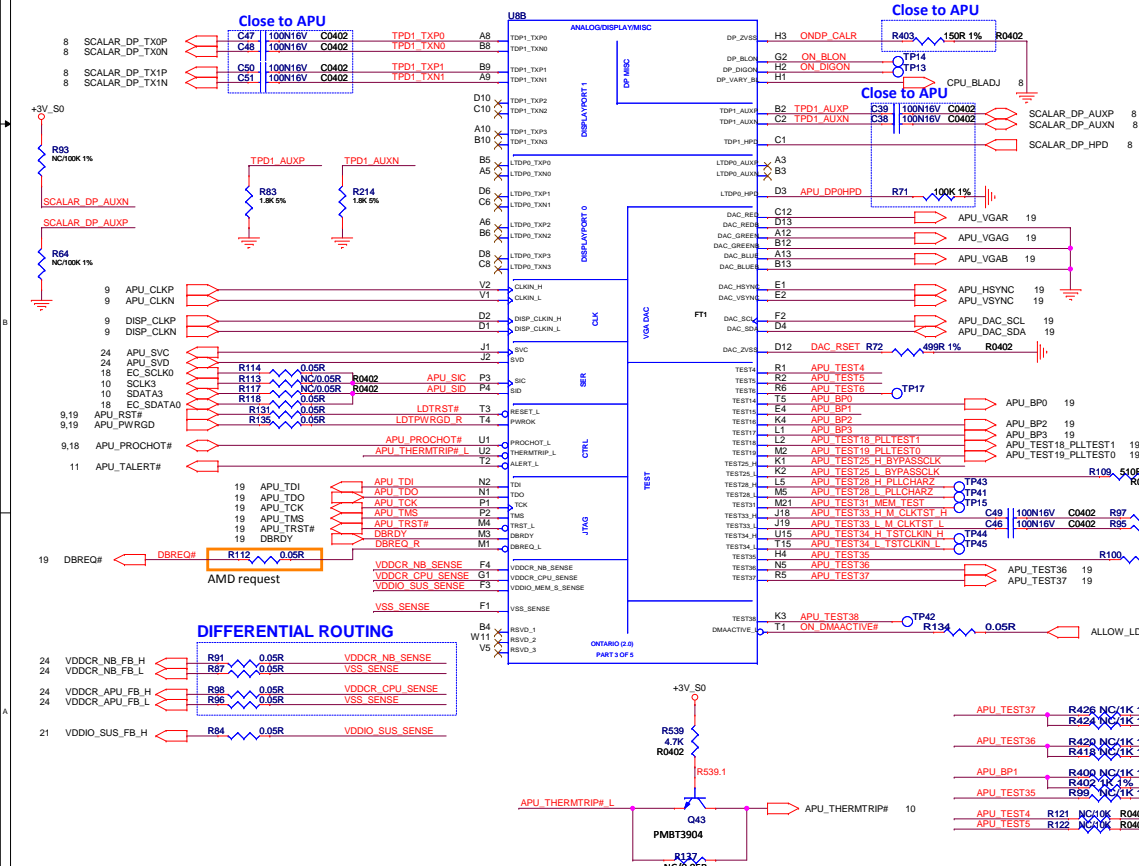
SM Bus MAP



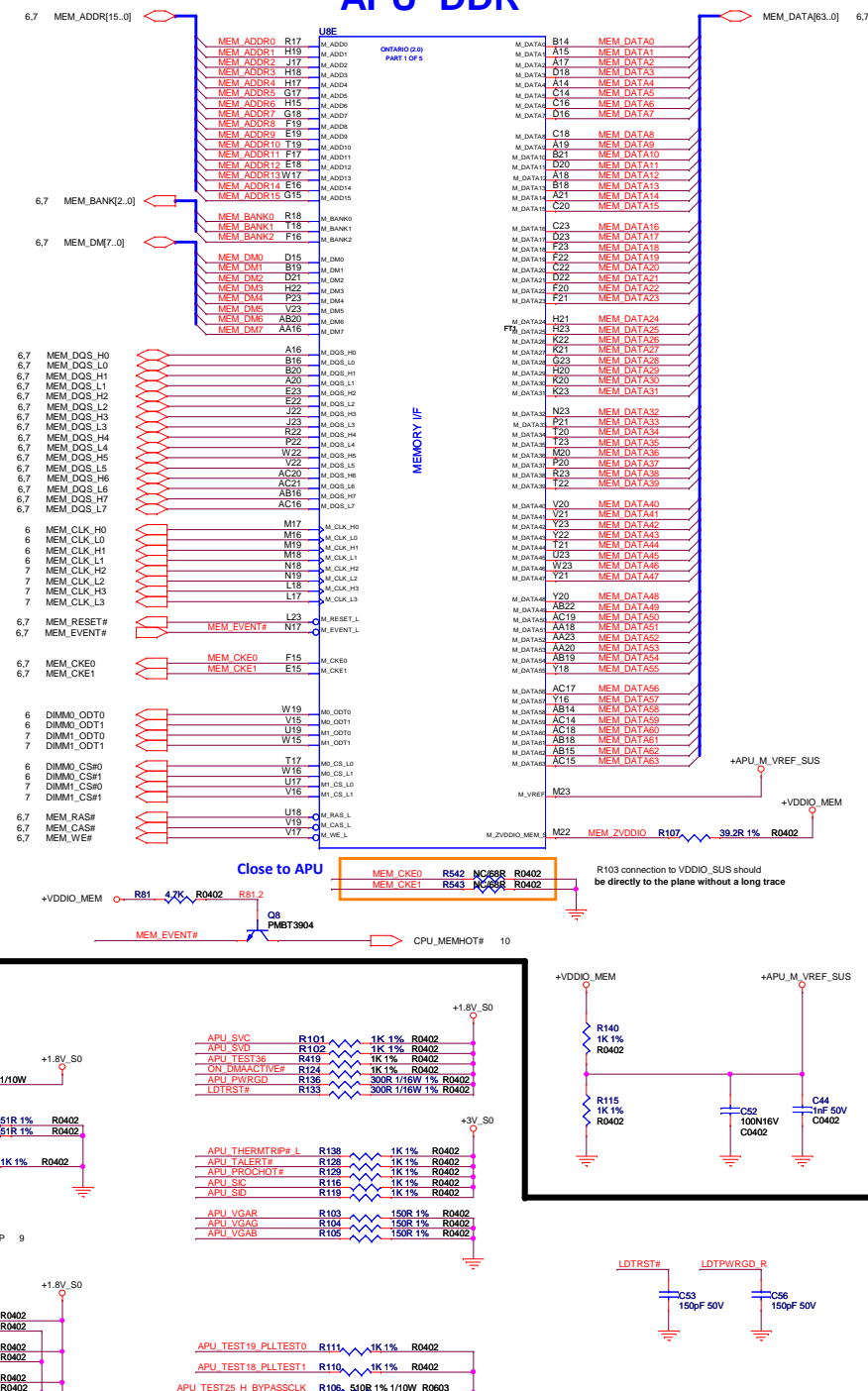
UMI / PCIE



DP / VGA / CLK / CTRL / SENSE

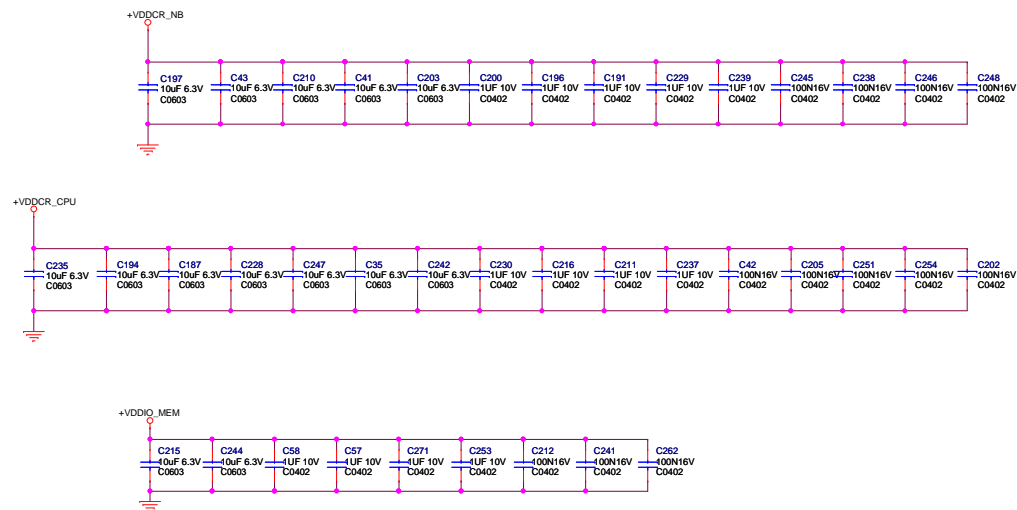
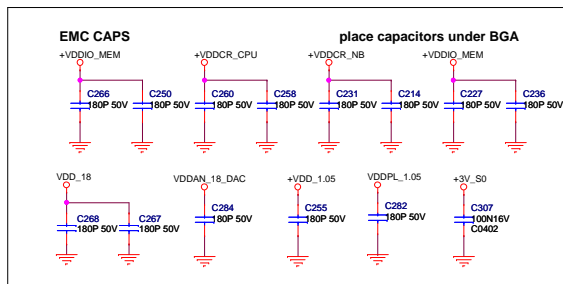
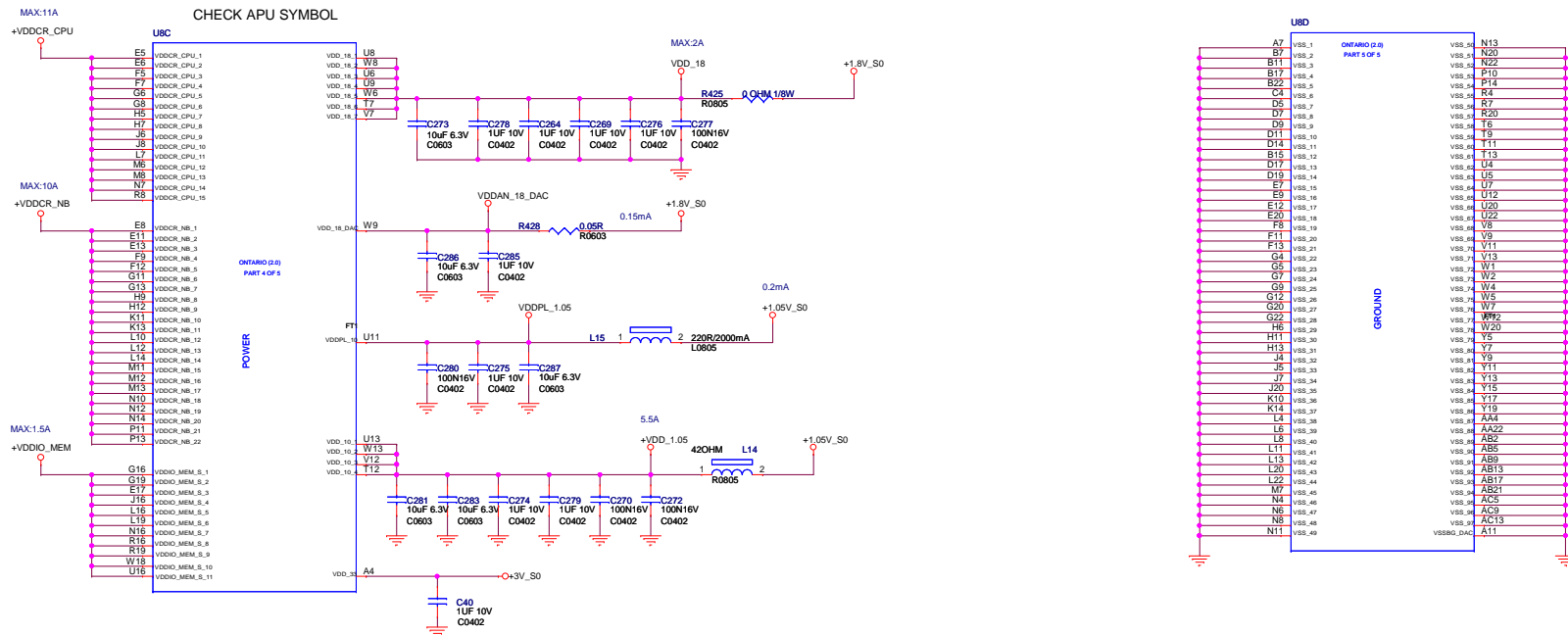


APU DDR

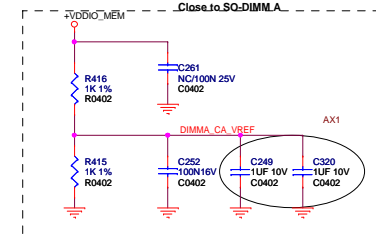
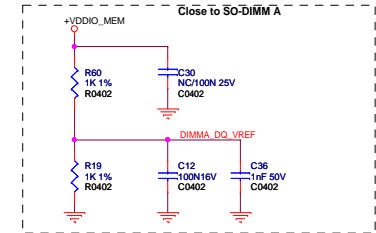
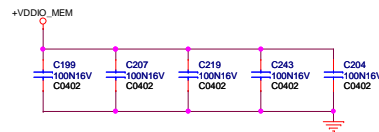
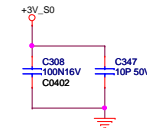
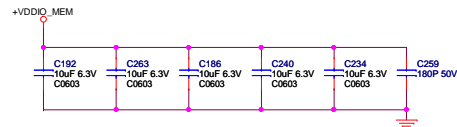
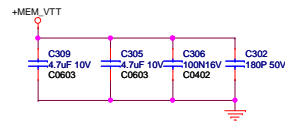
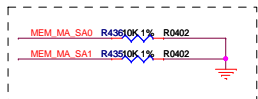
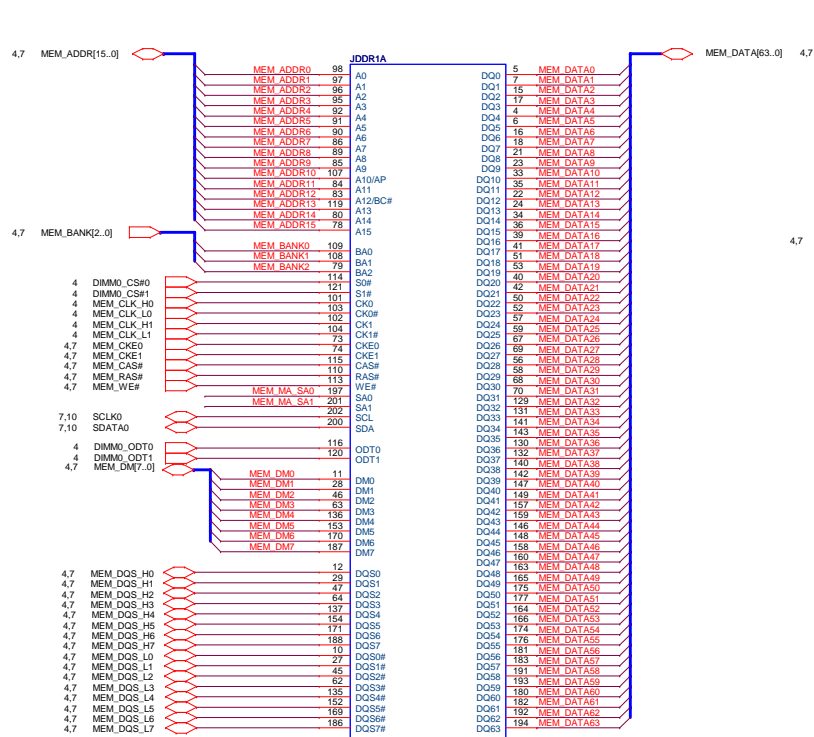


TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
線路圖編號 1		TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	0050A2516401	備註	<請註>
Date	Tuesday, May 22, 2012	Sheet	4 of 30		

CPU POWER / GND

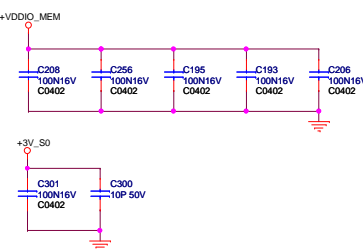
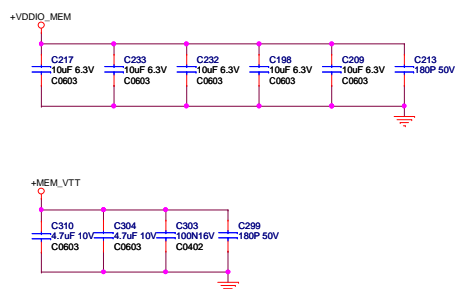
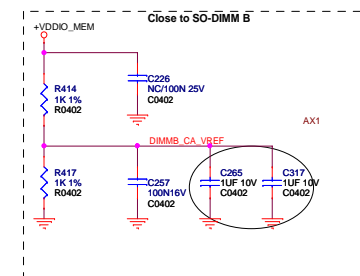
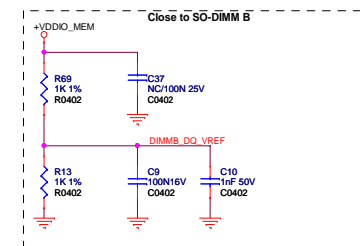
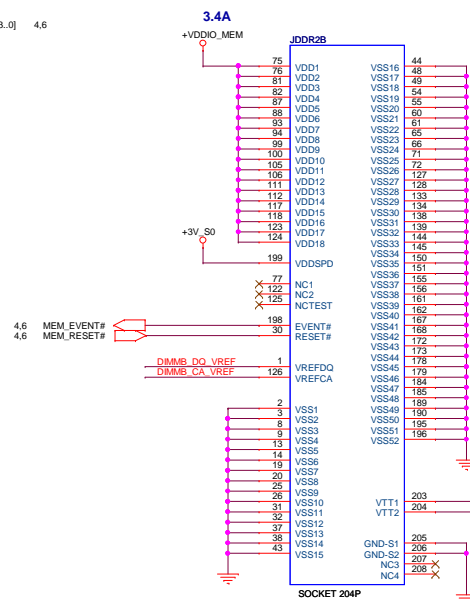
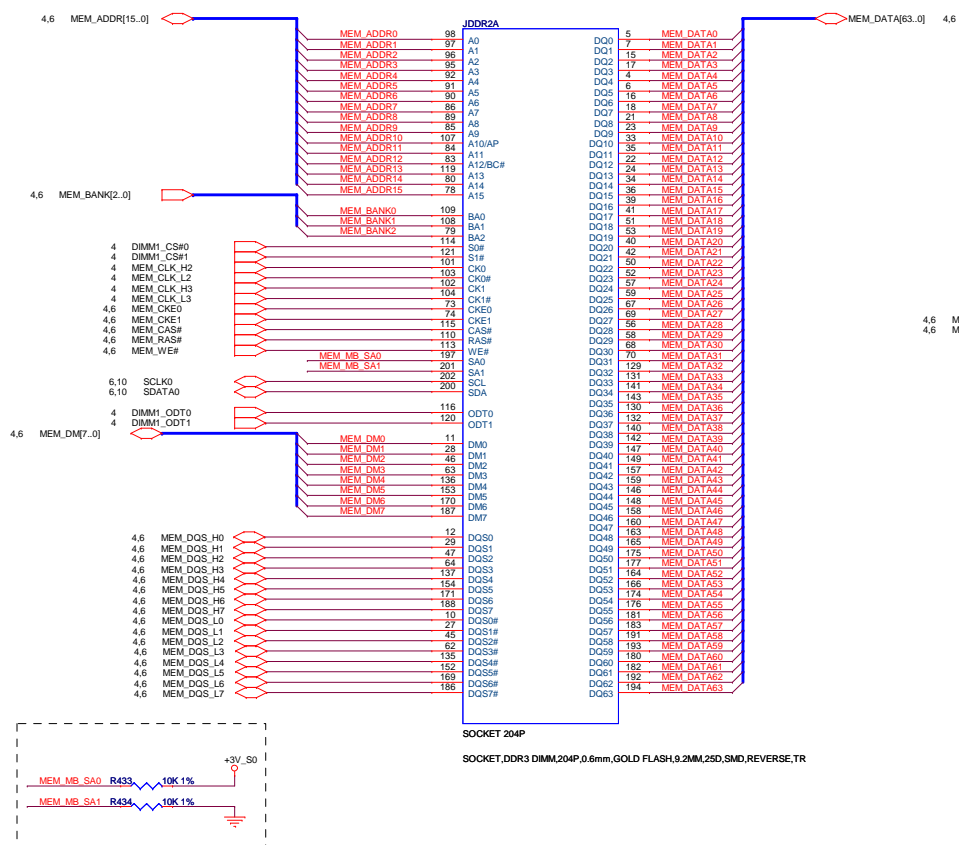


CHA DDR 5.2H(DIMM-0)

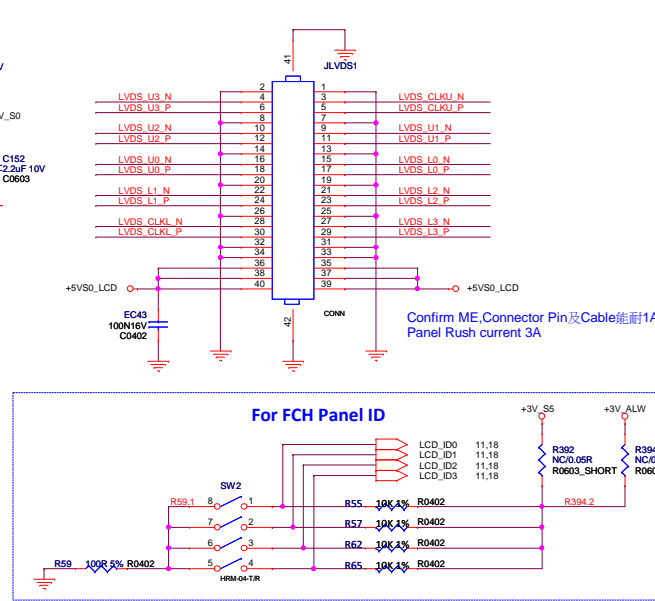
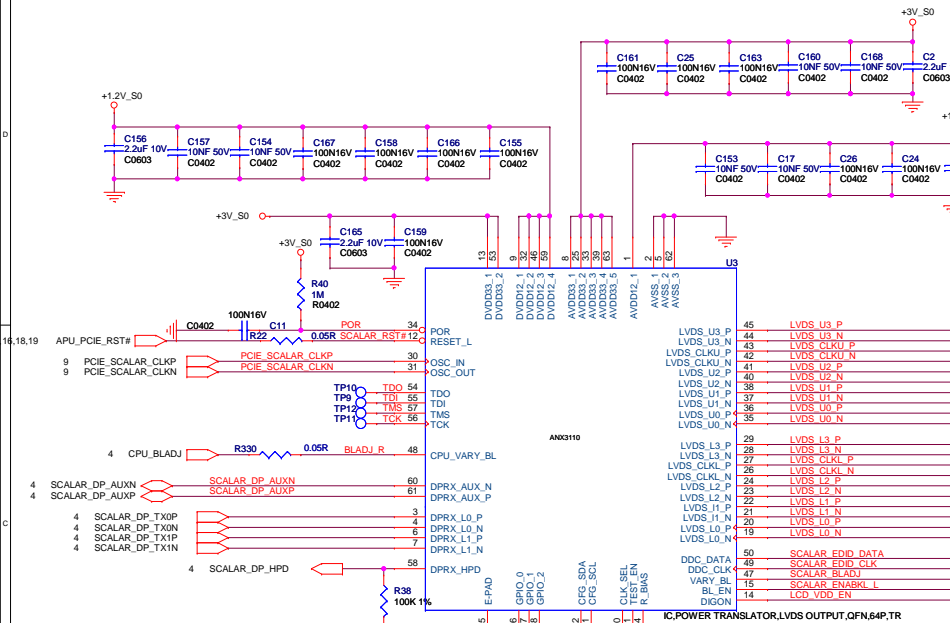


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Date	PCB NAME	6050A2516401	備註	<備註>
Tuesday, May 22, 2012	Sheet	6 of 30		

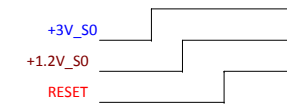
CHB DDR 9.2H(DIMM-1)



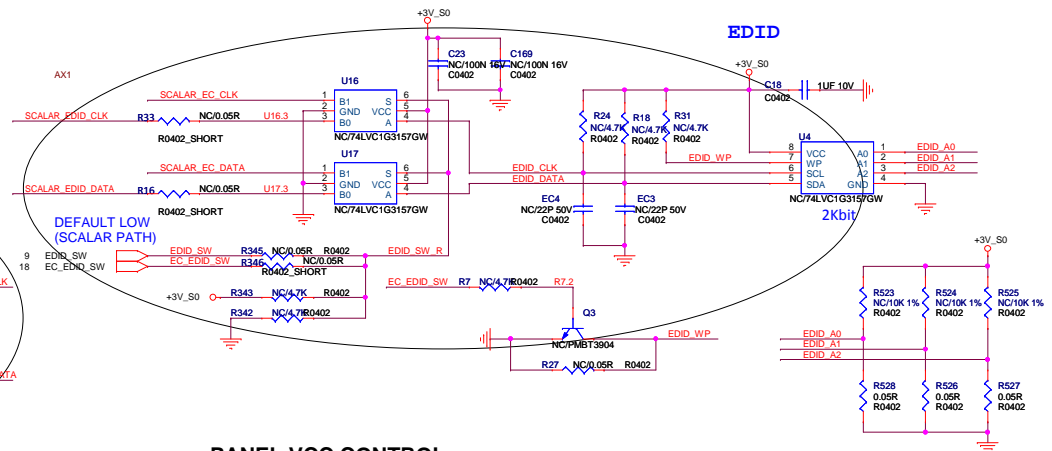
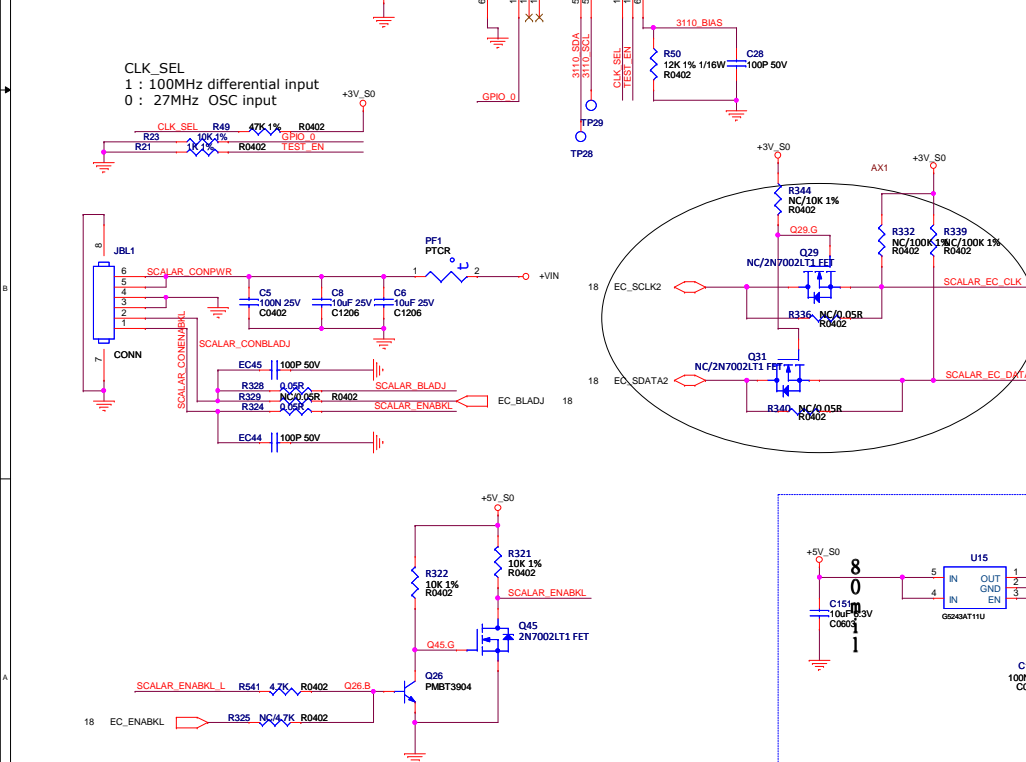
ANX3110



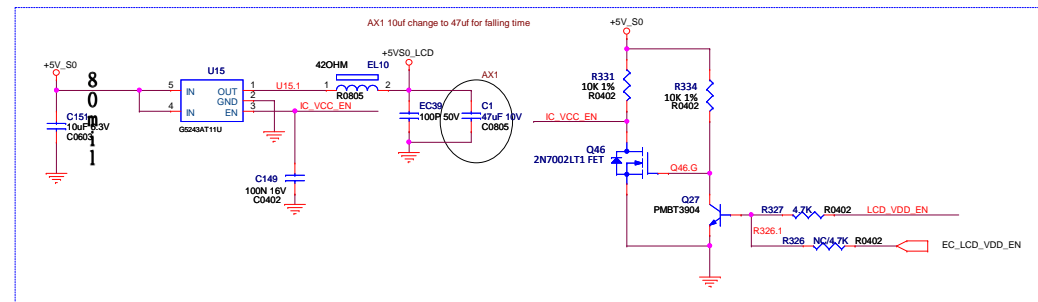
ANX3110 Power Sequence



	ID0	ID1	ID2	ID3
	0	0	0	0
	0	0	0	1
	0	0	1	0
	0	0	1	1
	0	1	0	0
	0	1	0	1
	0	1	1	0

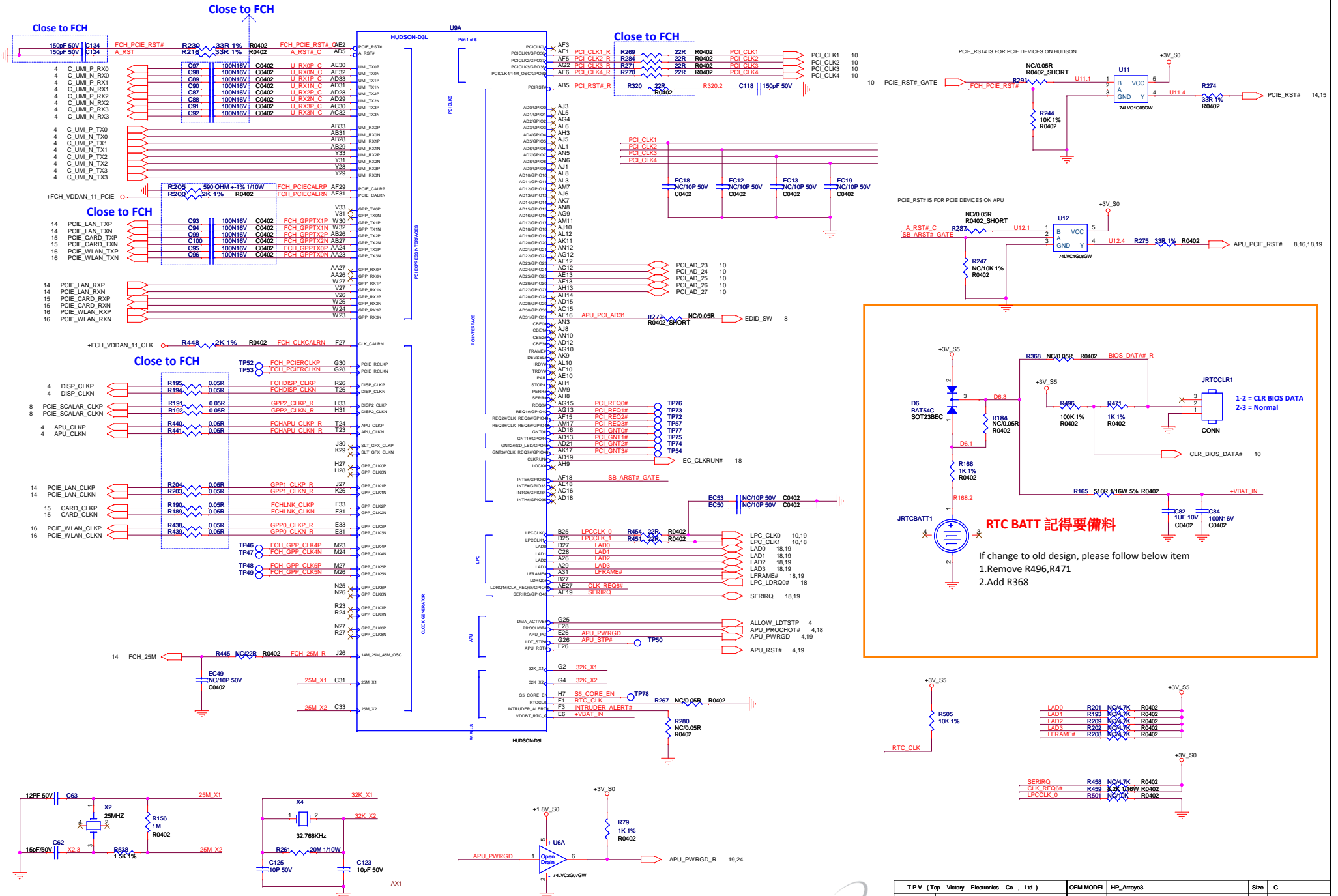


PANEL VCC CONTROL



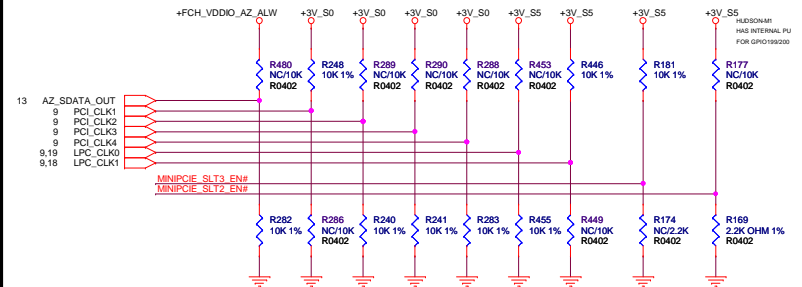
T P V (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
	1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401		
Date	Tuesday, May 22, 2012	Sheet	8 of 30		< >

UMI / PCIE / CLK / PCI I/F / LPC /RTC

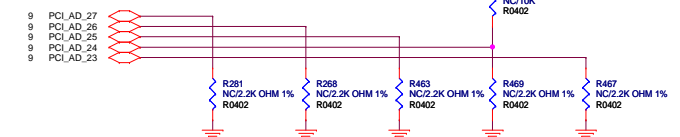


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
線路圖編號 1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	
Date	Tuesday 09/25/2019	Sheet	0 of 30	
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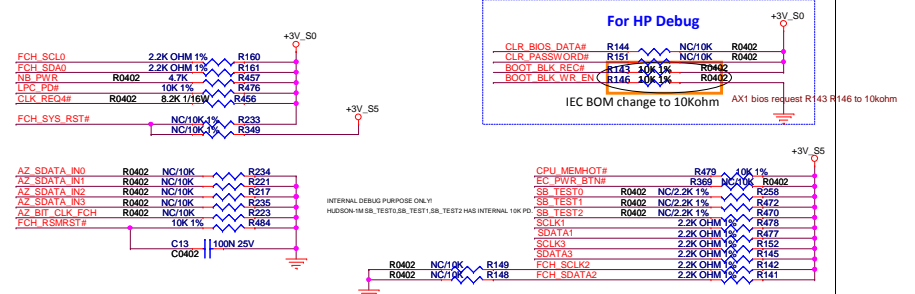
STRAP PINS



DEBUG STRAPS

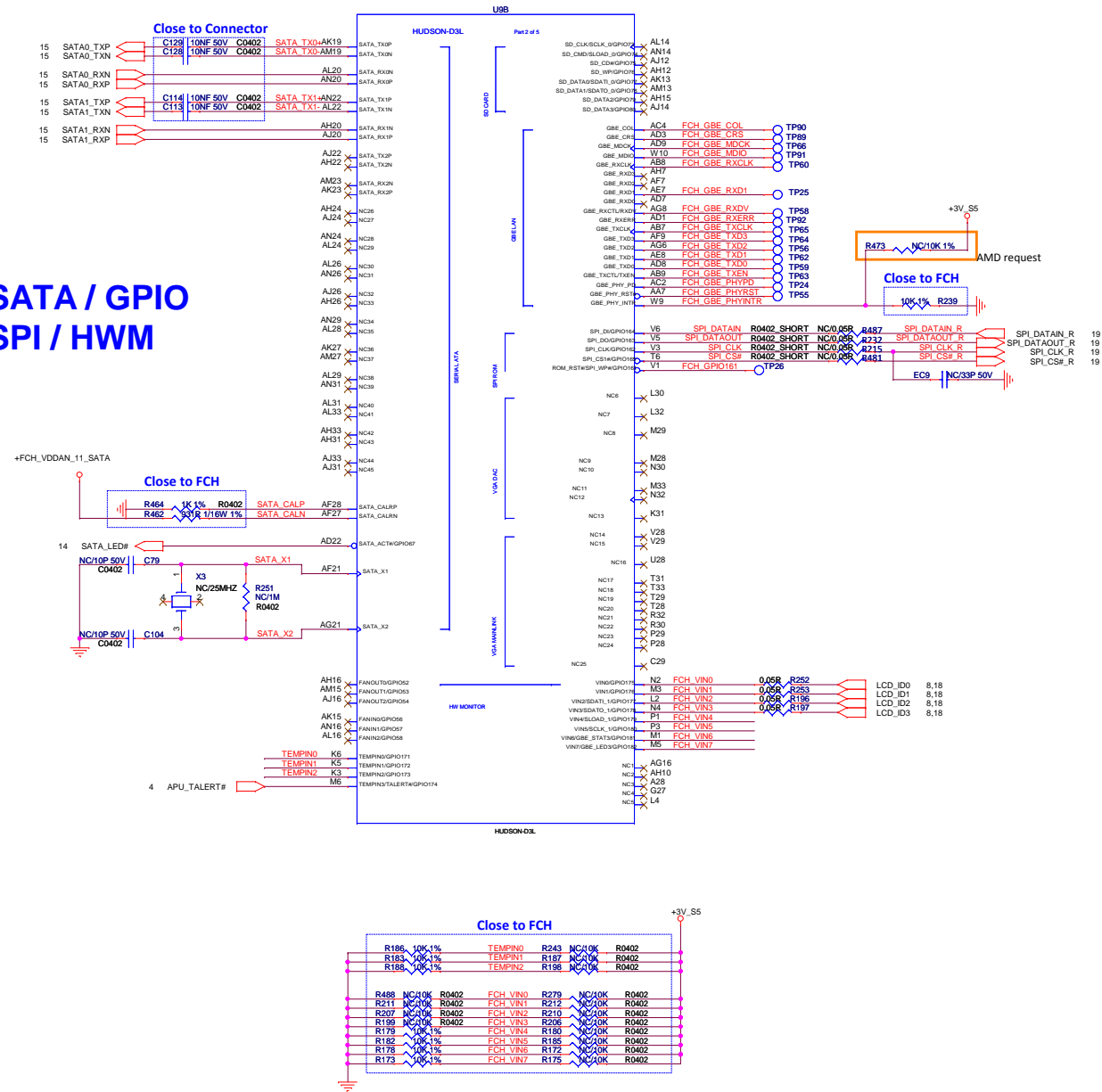


	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL "HIGH"	USE PCI PULL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE PCI PULL DEFAULT	USE DEFAULT PCI STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL "LOW"	BYPASS PCI PULL	ENABLE ILA AUTORUN	BYPASS PCI PULL	USE EEPROM PCI STRAPS	ENABLE PCI MEM BOOT



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
線路圖編號	1	TPV MODEL	Arroyo3	Rev
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	A02
Date	Tuesday, May 22, 2019	Sheet	10 of 30	備註 <備註>

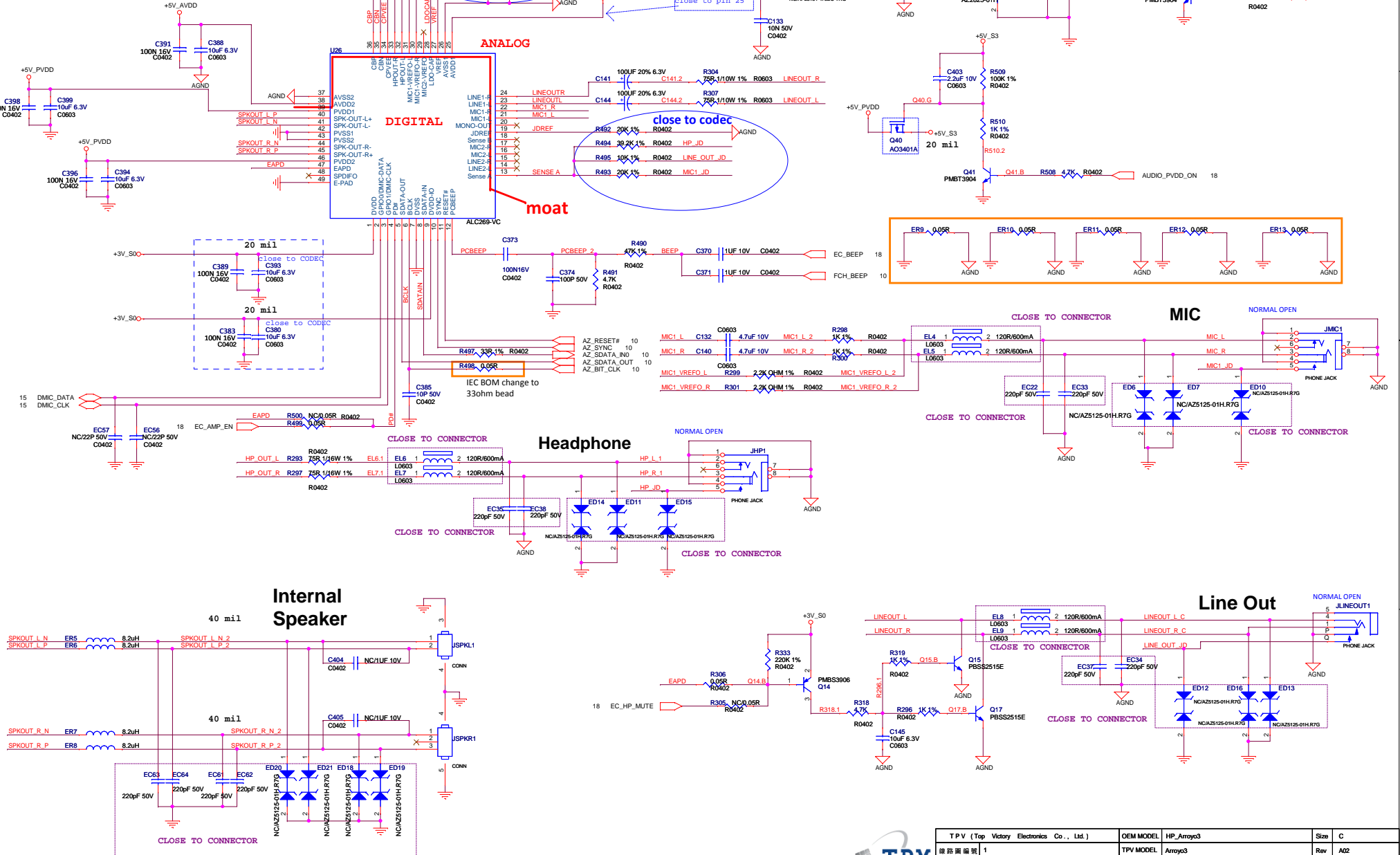
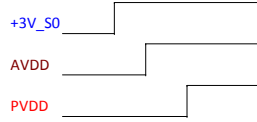
SATA / GPIO SPI / HWM



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Table of Contents	PCB NAME	6050A2516401	備註	<備註>
Date	Sheet	11 of 30		

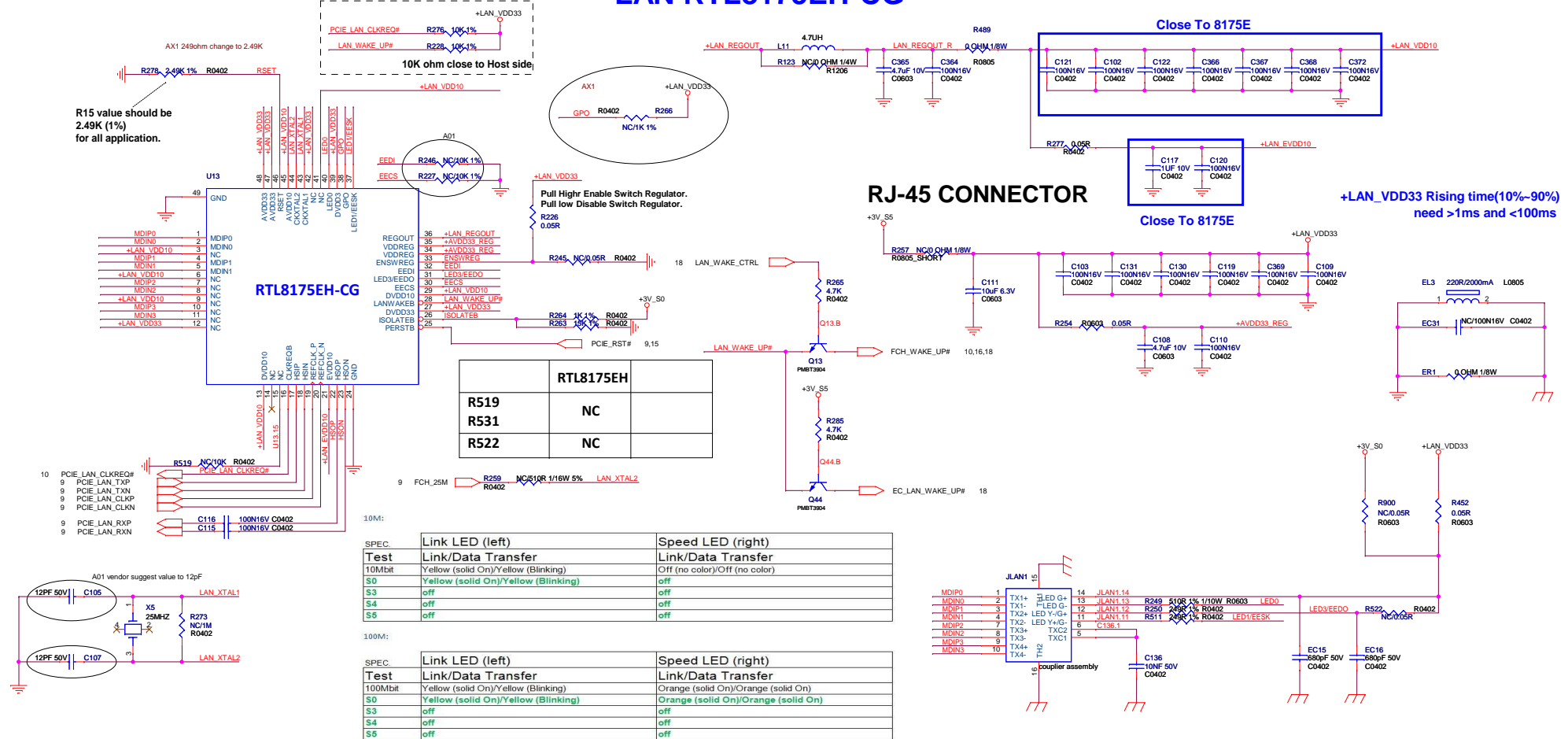
AUDIO CODEC (ALC269-VC)

Power Sequence

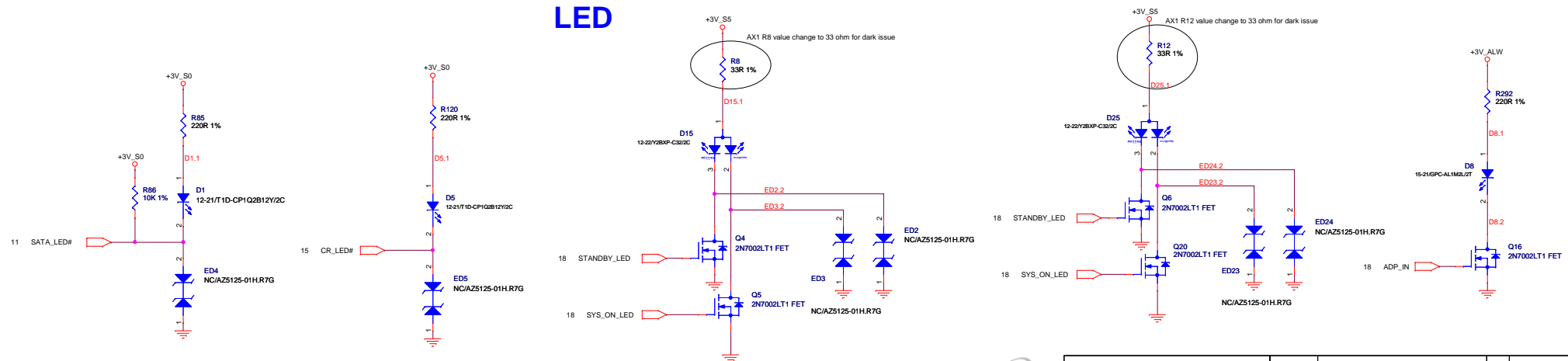


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
TABLE OF CONTENTS	PCB NAME	6050A2516401	Sheet	13 of 30
Date	Tuesday, May 22, 2012			備註 <備註>

LAN RTL8175EH-CG

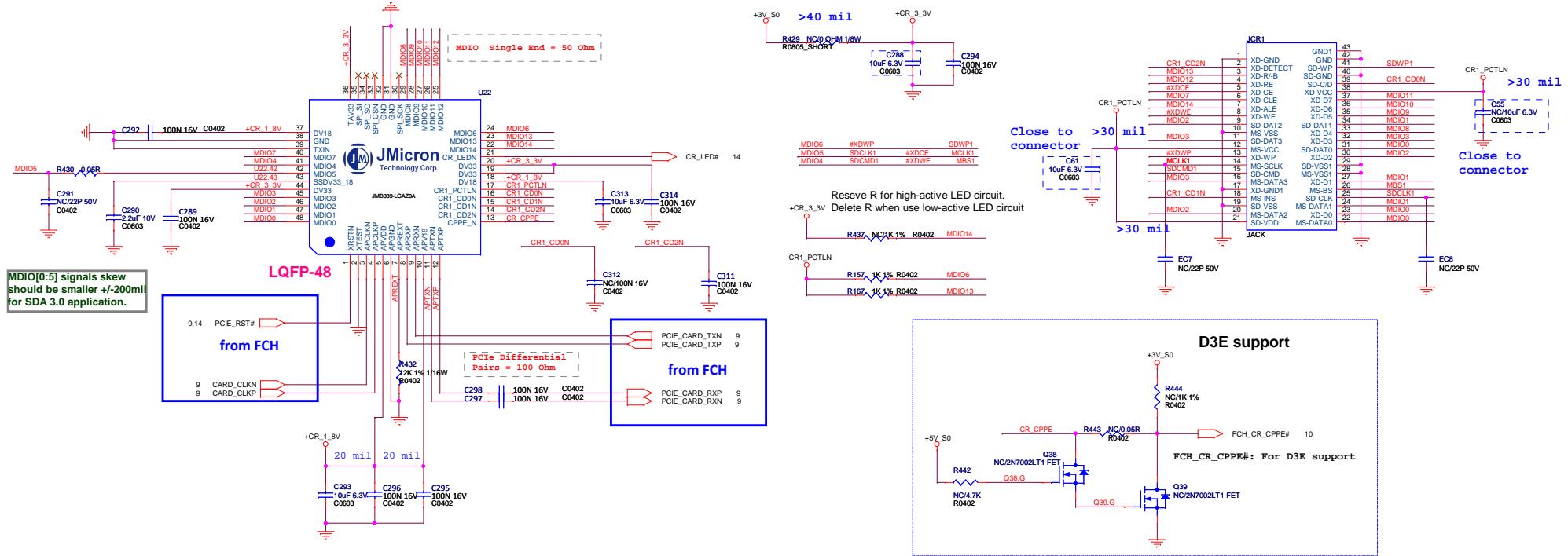


LED



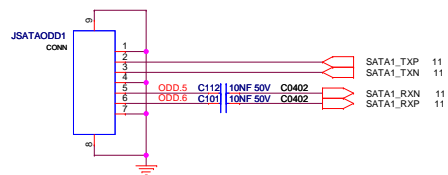
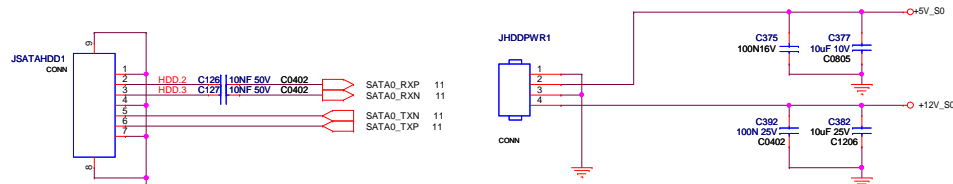
TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
線路圖編號 1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	
Date	Tuesday, May 22, 2012	Sheet	14 of 30	備註 <備註>

Card Reader JMB389A



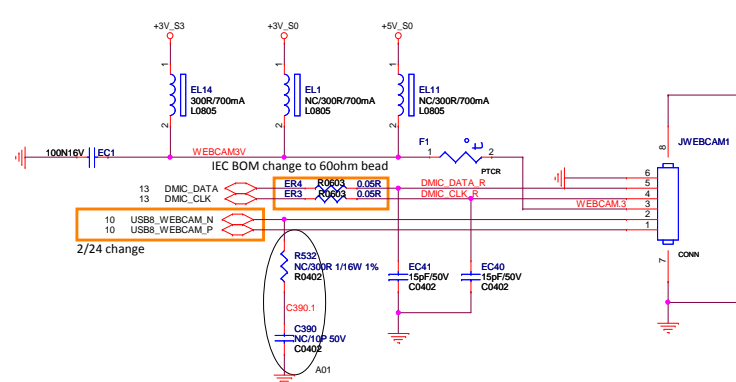
HDD/ODD

HDD POWER (3.5")



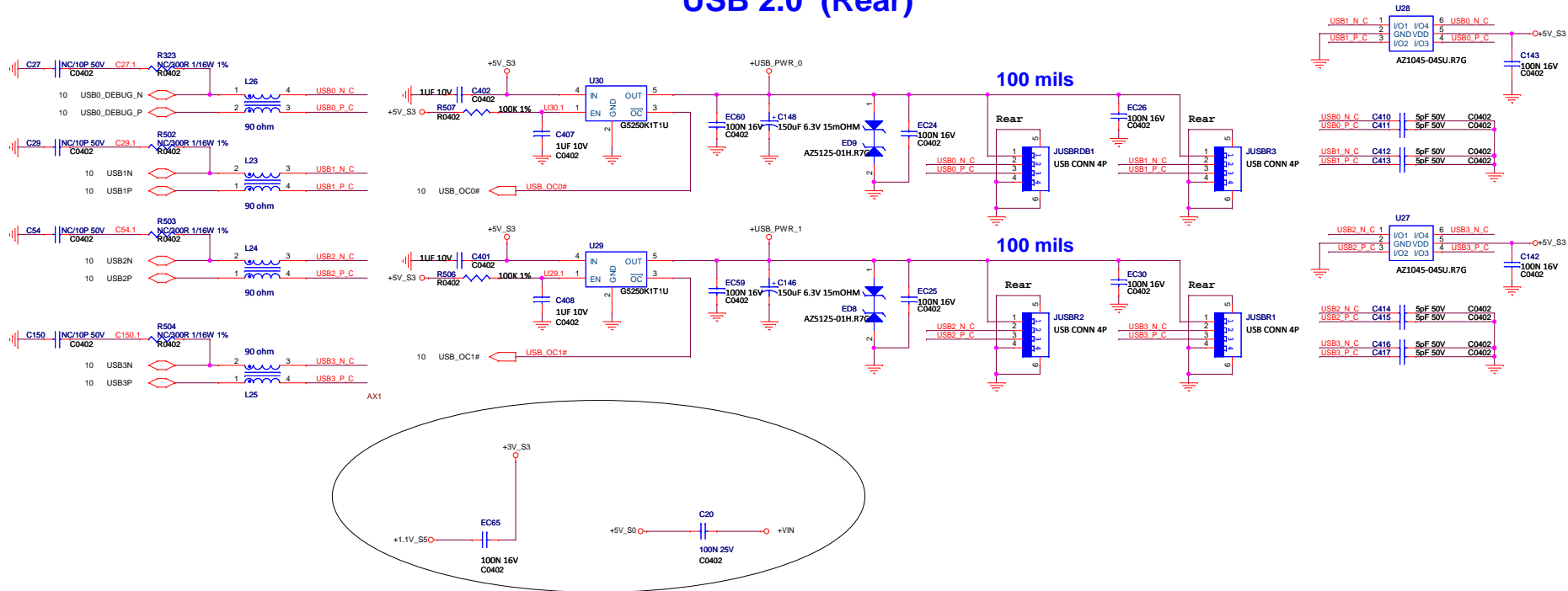
ODD POWER

WEB CAM / D-MIC

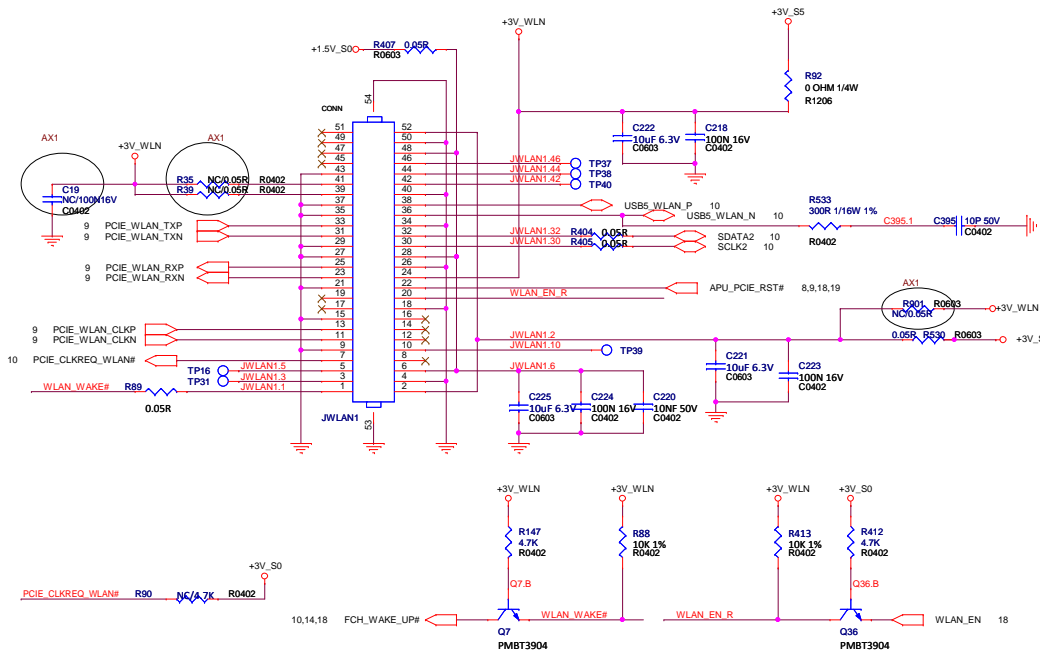


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
TABLE OF CONTENTS	PCB NAME	6050A2516401	備註	<備註>
Date	Sheet	15 of 30		

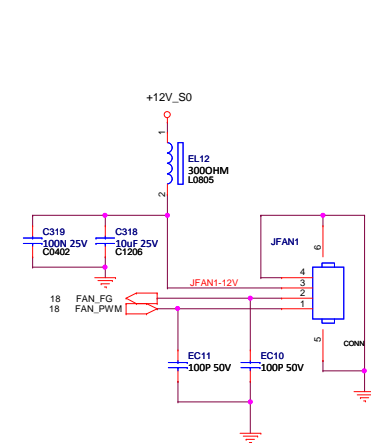
USB 2.0 (Rear)



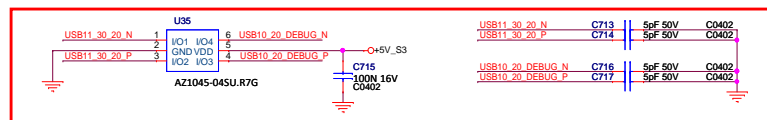
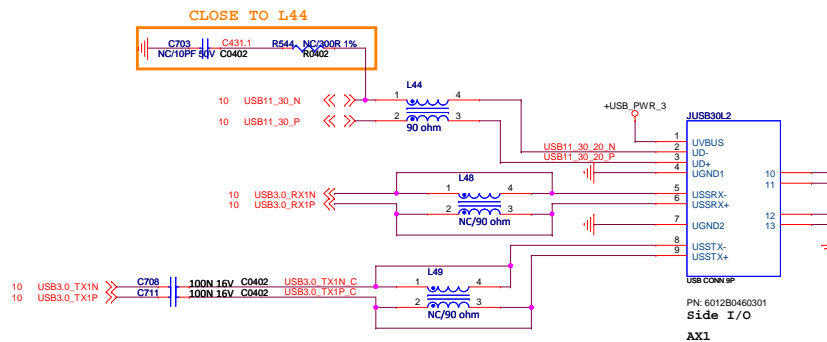
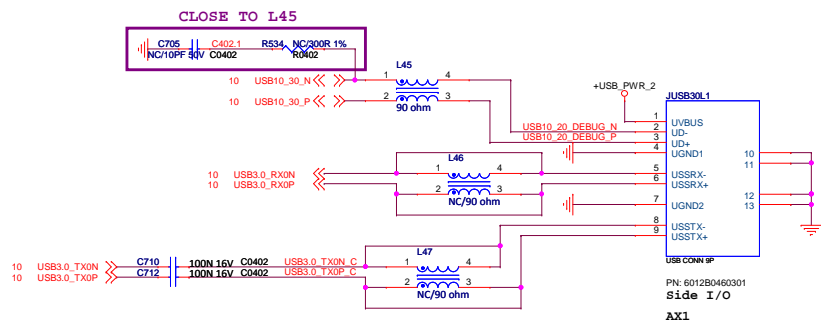
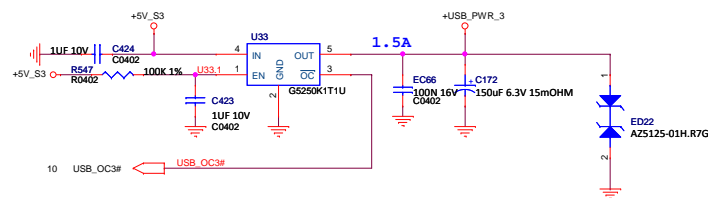
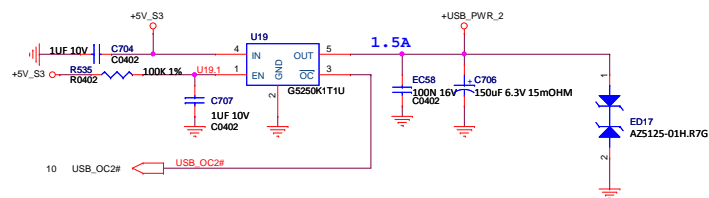
MINI PCI-E WLAN & BT



CPU Linear FAN Control

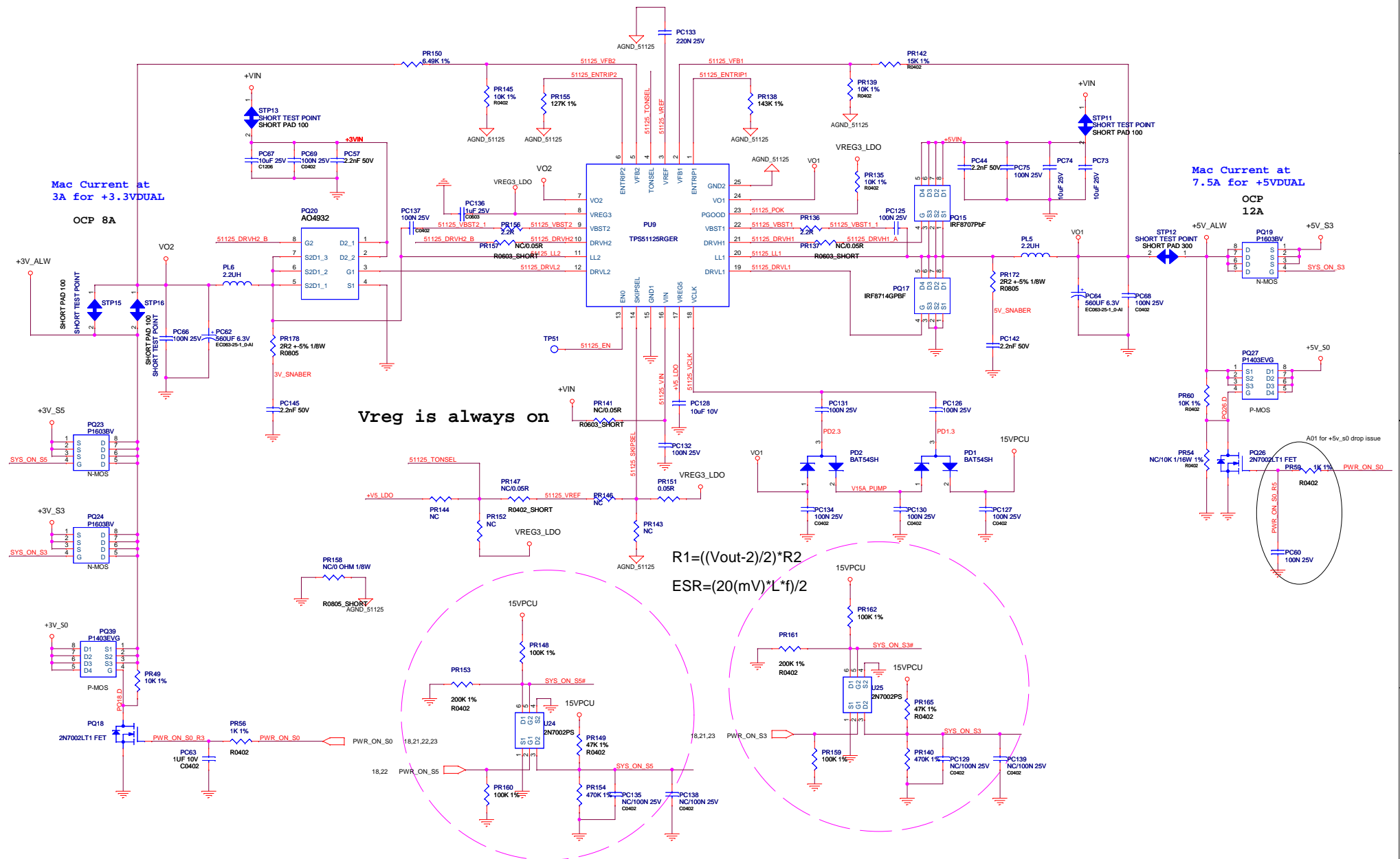


USB 3.0 (Side IO)



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
1	TPV MODEL	Arroyo3	Rev	A02
Key Component	PCB NAME	6050A2516401	備註	<備註>
Date	Sheet	17 of 30		

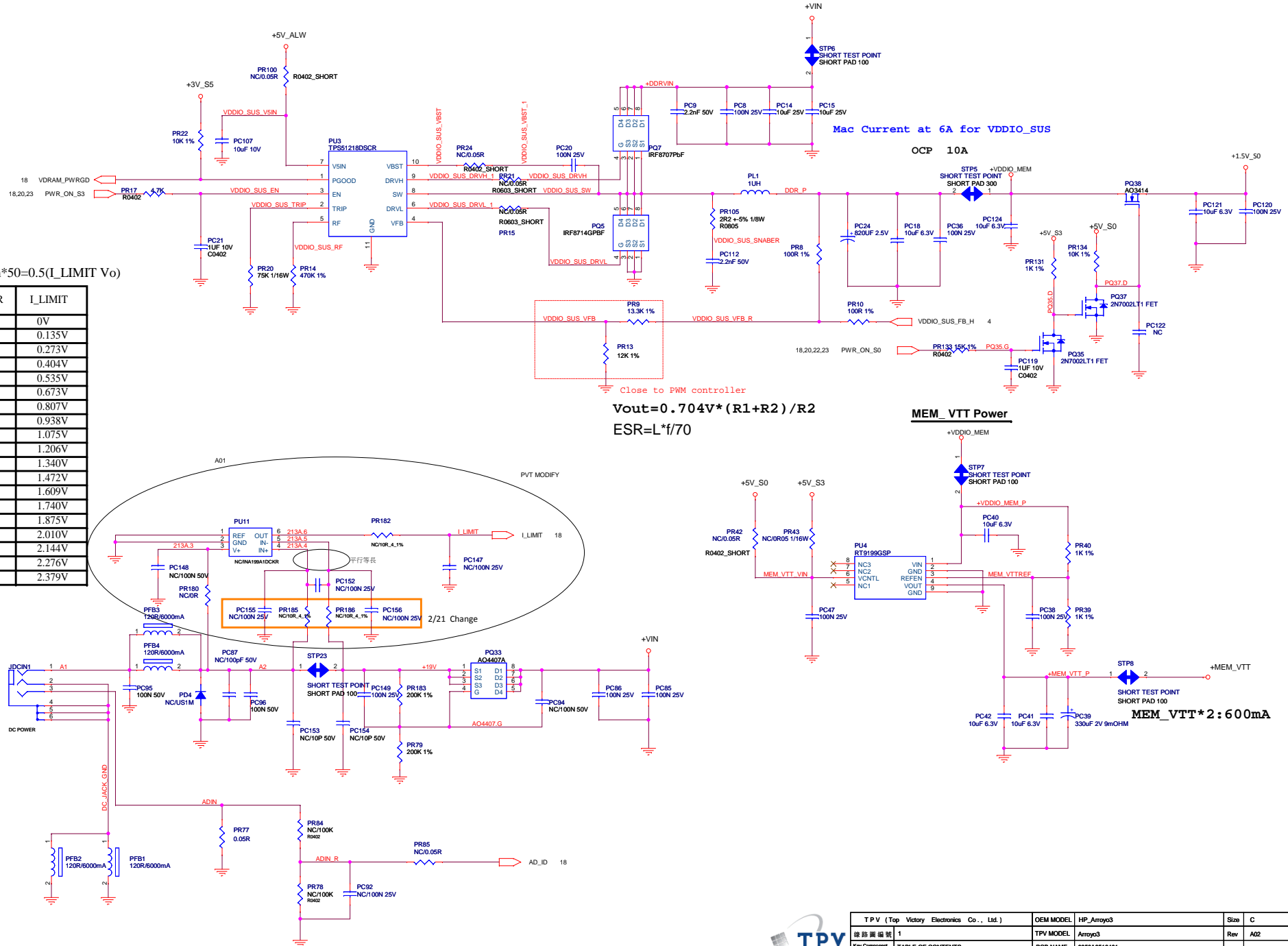
**SYSTEM +3V_ALW/+3V_S5/+3V_S3/+3V_S0
+5V_ALW/+5V_S3/+5V_S0**



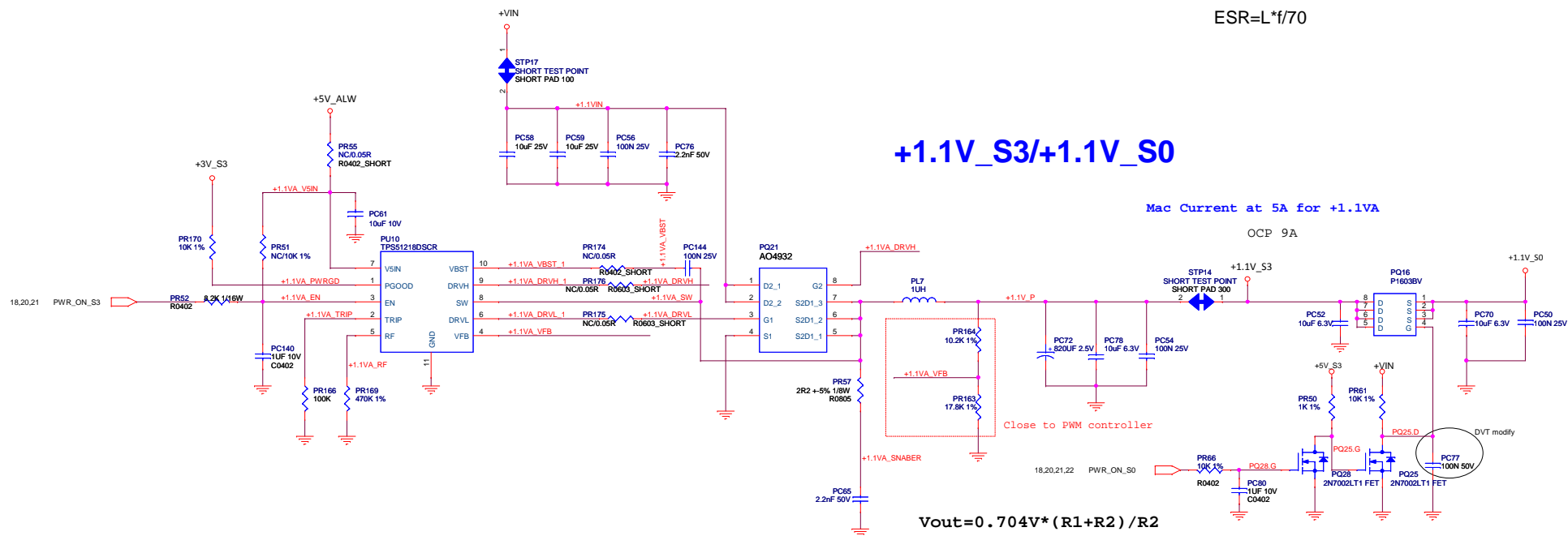
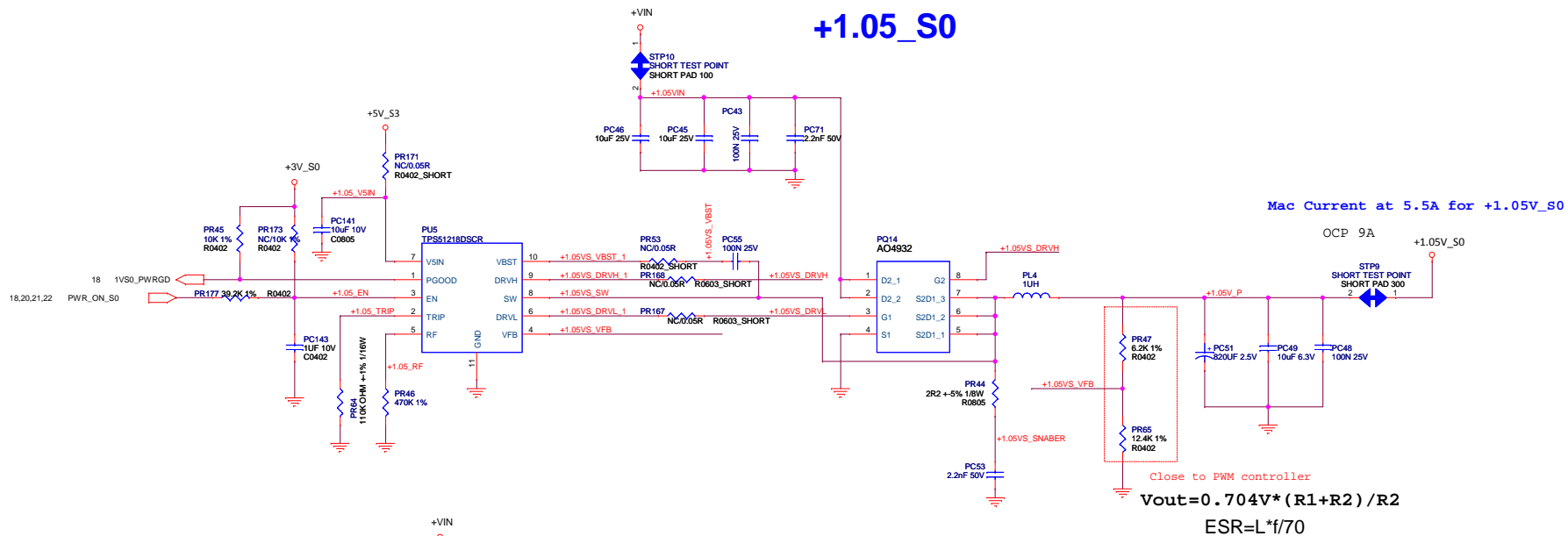
VDDIO_SUS, MEM_VTT, DC-IN

1A*.01ohm*50=0.5(I_LIMIT Vo)

TOTAL POWER	I_LIMIT
0W	0V
5W	0.135V
10W	0.273V
15W	0.404V
20W	0.535V
25W	0.673V
30W	0.807V
35W	0.938V
40W	1.075V
45W	1.206V
50W	1.340V
55W	1.472V
60W	1.609V
65W	1.740V
70W	1.875V
75W	2.010V
80W	2.144V
85W	2.276V
90W	2.379V

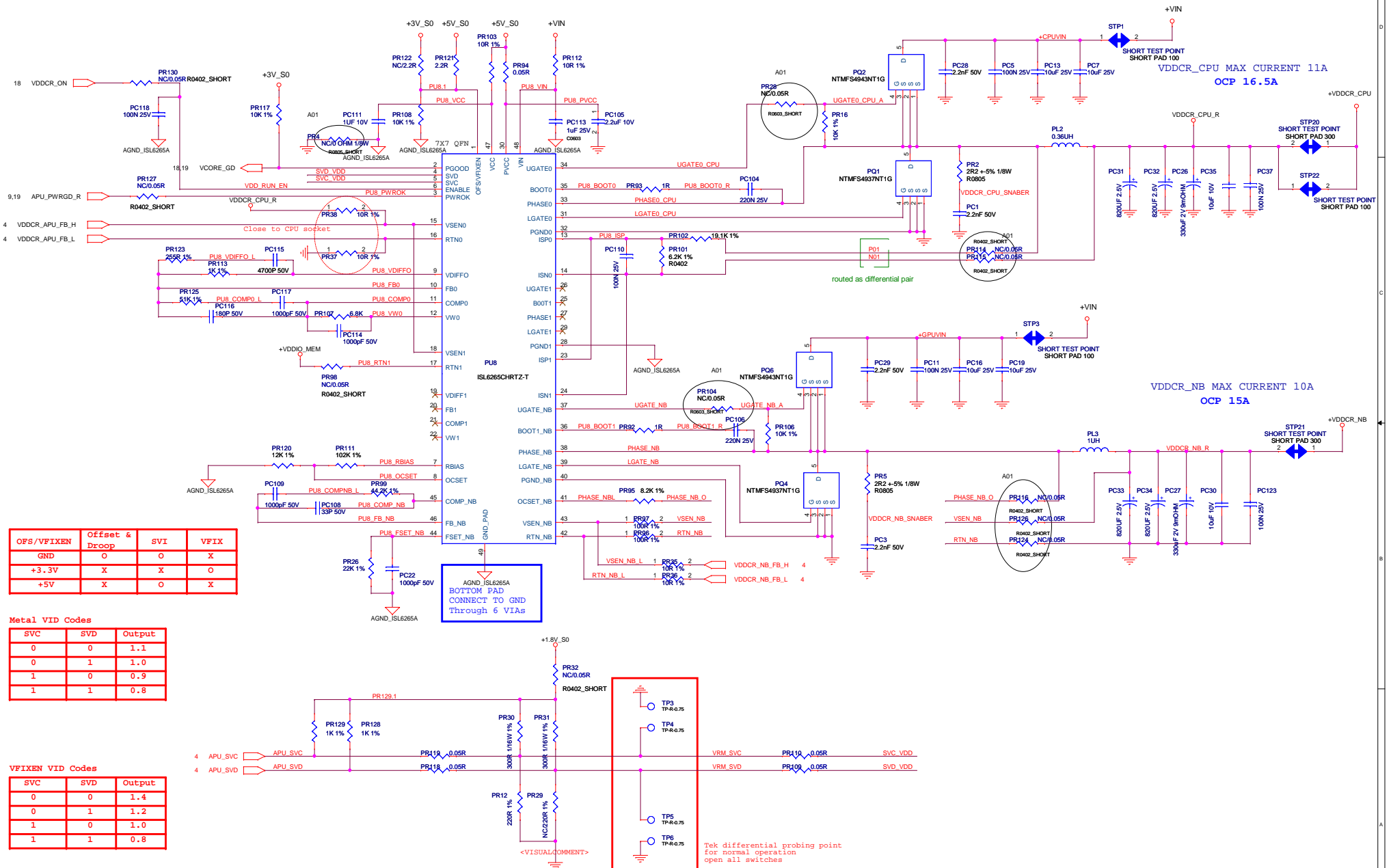


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
TPV MODEL	Arroyo3		Rev	A02
PCB NAME	6050A2516401			
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TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Date	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註
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VDDCR_CPU & VDDCR_NB



STACK UP 6L

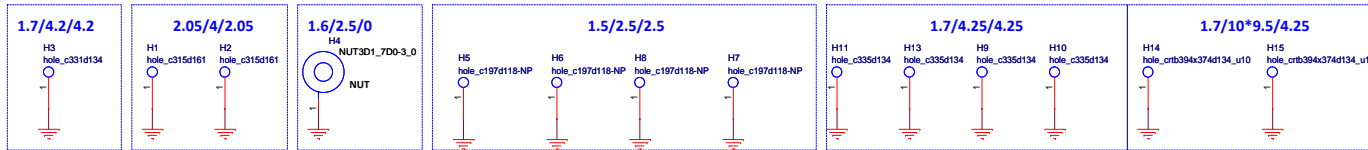
Stack Up									
PCB Stack Up					Impedance				
Layer	Type	Thickness (mil)							
Top side solder mask			1.00 mils		Single end	理論値	differential	理論値	
L1	TOP	copper+plating	1.58 mils		4 mils , 50Ω±15% 8 mils , 40Ω ±10% 10 mils , 35Ω ±15%	53.94Ω 38.75Ω 34.22Ω	5/6/5 mils , 85Ω±10% 4/8/4 mils , 100Ω ±10% 4/5/4 mils , 90Ω ±10% 7/5/7 mils , 72Ω ±10%	83.03Ω 97.18Ω 86.49Ω 70.72Ω	
		Prepreg	3.50	mils					
L2		copper	1.30 mils						
		core	4.00 mils						
L3	IN1	copper	0.70 mils		4 mils , 50Ω±15% 8.5 mils , 40Ω ±10% 10 mils , 35Ω ±15%	56Ω 39.1Ω 35.68Ω	5/5/5 mils , 85Ω±10% 4/7/4 mils , 100Ω ±10% 5/6/5 mils , 90Ω ±10% 7.5/5/7.5 mils , 72Ω ±10%	84.33Ω 98.09Ω 87.21Ω 70.39Ω	
		Prepreg+core	36.00	mils					
L4	IN2	copper	0.70 mils		4 mils , 50Ω±15% 8.5 mils , 40Ω ±10% 10 mils , 35Ω ±15%	56Ω 39.1Ω 35.68Ω	5/5/5 mils , 85Ω±10% 4/7/4 mils , 100Ω ±10% 5/6/5 mils , 90Ω ±10% 7.5/5/7.5 mils , 72Ω ±10%	84.33Ω 98.09Ω 87.21Ω 70.39Ω	
		core	4.00 mils						
L5		copper	1.30 mils						
		Prepreg	3.50	mils					
L6	Bottom	copper+plating	1.58 mils		4 mils , 50Ω±15% 8 mils , 40Ω ±10% 10 mils , 35Ω ±15%	53.94Ω 38.75Ω 34.22Ω	5/6/5 mils , 85Ω±10% 4/8/4 mils , 100Ω ±10% 4/5/4 mils , 90Ω ±10% 7/5/7 mils , 72Ω ±10%	83.03Ω 97.18Ω 86.49Ω 70.72Ω	
Bottom side solder mask			1.00 mils						
TOTAL			60.16 mils						
			1.53 mm						



TPV (Top Victory Electronics Co., Ltd.)		OEM MODEL	HP_Arroyo3	Size	C
Key Component	1	TPV MODEL	Arroyo3	Rev	A02
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SCREW HOLE PLACEMENT

內徑/外徑(T)/外徑(B)



PCB SILK SCREEN

PCB color codes:

EVT (Red); Silkscreen (White)

DVT (Blue); Silkscreen (White)

PVT (Green); Silkscreen (Yellow)

Production (Green); Silkscreen (White)

HW_PWRGD --> D713,G (PS_ON#)

VCORE_GD --> D721,G (VRM_PWRGD)

FCH_PWRGD --> D715,G (POWERGOOD_140MS)

AUX_POWER --> D720,G

SLP_S3 --> D716,G

SLP_S5 --> D717,G

AMD HDT --> JHDT1

LPC HEADER --> JLPADB1

ROM Recovery --> JHPDB5

BBW_EN --> JHPDB4

USB WinDbg --> JUSBRDB1

DIMM0 --> JDDR52

DIMM1 --> JDDR92

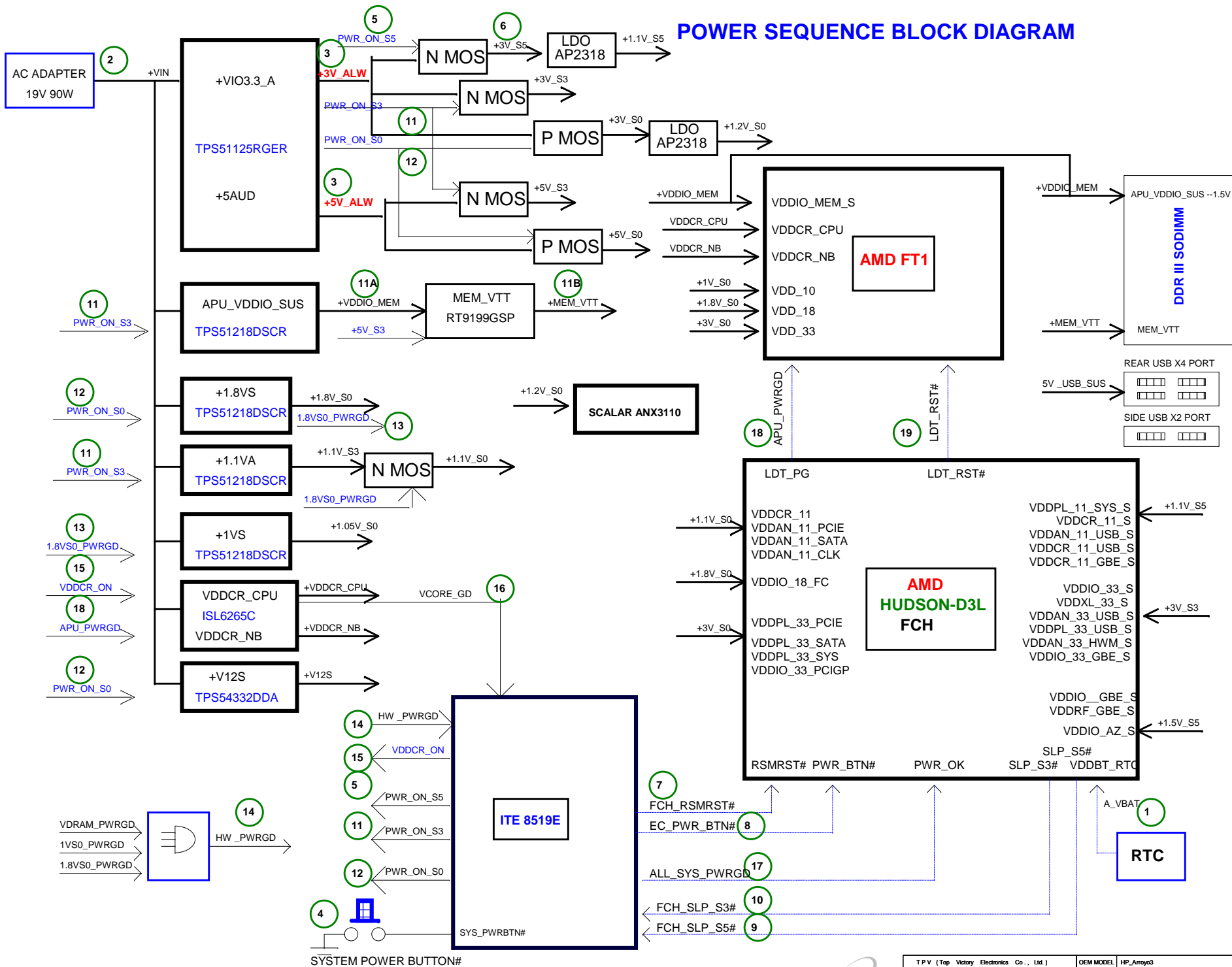
T&I PCB Version = X01 , AX1 , AX2.....

MP = A01 , A02.....

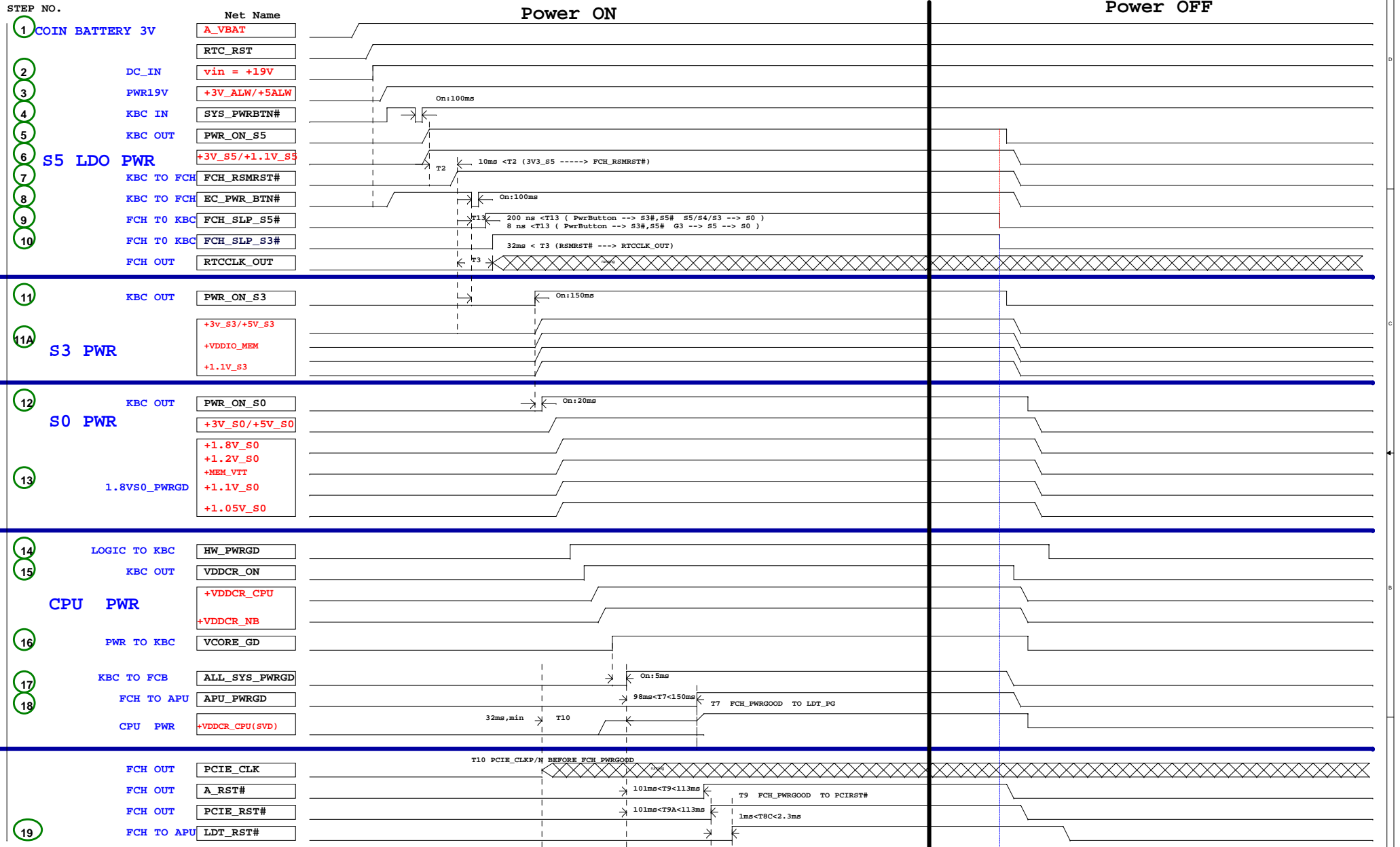


TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
1	TPV MODEL	Arroyo3	Rev	A02
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POWER SEQUENCE BLOCK DIAGRAM



POWER SEQUENCE



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
Key Component	TPV MODEL	Arroyo3	Rev	A02
Date	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註
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Hudson D3L Fusion Controller HubGPIO Table

PIN NAME	PIN#	POWER WELL	USAGE	Default Type	Default Value	During Reset	S0	S3	S4/S5	Enable Setting	Note
WD0161	V1	RESNONE	FCM_VIO161	GPIO	LOW	Output LOW					
WD0162	V3	RESNONE	SP1_CLK	SP1_CLK	LOW						Internal PD 10K
WD0163	V5	RESNONE	SP1_DATA07	SP1_DO	LOW						Internal PD 10K
WD0164	V6	RESNONE	SP1_DATA14	SP1_DI	LOW						Internal PD 10K
WD0165	V6	RESNONE	SP1_CS#	SP1_CS#	HIGH						Internal PD 10K
WD0166	V21	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0167	A2	RESNONE	AZ_SDATA_100	AZ_SDIO10	LOW						Internal PD 50K
WD0168	V5	RESNONE	AZ_SDATA_101	AZ_SDIO1	LOW						Internal PD 50K
WD0169	V3	RESNONE	AZ_SDATA_102	AZ_SDIO2	LOW						Internal PD 50K
WD0170	V1	RESNONE	AZ_SDATA_103	AZ_SDIO3	LOW						Internal PD 50K
WD0171	V6	RESNONE	TRMP100	GPIO							
WD0172	K5	RESNONE	TRMP101	GPIO							
WD0173	K3	RESNONE	TRMP102	GPIO							
WD0174	M6	RESNONE	APU_VIA107#	GPIO							
WD0175	N2	RESNONE	FCM_VIO10	GPIO							Internal PD/PD(disable by default) 10K
WD0176	M3	RESNONE	FCM_VIO1	GPIO							Internal PD/PD(disable by default) 10K
WD0177	V2	RESNONE	FCM_VIO2	GPIO							Internal PD/PD(disable by default) 10K
WD0178	N4	RESNONE	FCM_VIO3	GPIO							Internal PD/PD(disable by default) 10K
WD0179	V1	RESNONE	FCM_VIO4	GPIO							Internal PD/PD(disable by default) 10K
WD0180	V3	RESNONE	FCM_VIO5	GPIO							Internal PD/PD(disable by default) 10K
WD0181	M1	RESNONE	FCM_VIO6	GPIO							Internal PD/PD(disable by default) 10K
WD0182	M5	RESNONE	FCM_VIO7	GPIO							
WD0183	M8	RESNONE	NC	GPIO							
WD0184	J2	RESNONE	NC	GPIO	HIGH						Internal PD 10K
											Internal PD 10K
WD0187	K19	RESNONE	FCM_P2S_DAT	GPIO	HIGH						Internal PD 10K
WD0188	J19	RESNONE	FCM_P2S_CLK	GPIO	HIGH						Internal PD 10K
WD0189	D21	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0190	C20	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0191	D23	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0192	C22	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0193	M19	RESNONE	FCM_SCLK2	GPIO	Tri-State						
WD0194	G19	RESNONE	FCM_SDATA2	GPIO	Tri-State						
WD0195	D22	RESNONE	SCLK3	GPIO	Tri-State						
WD0196	G21	RESNONE	SDATA3	GPIO	Tri-State						
WD0197	K22	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0198	K23	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0199	J22	RESNONE	MINIPIC1E_SLT3_EN#	GPIO	HIGH						Internal PD 10K
WD0200	K21	RESNONE	MINIPIC1E_SLT3_EN#	GPIO	HIGH						Internal PD 10K
WD0201	J21	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0202	J22	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0203	J22	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0204	J24	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0205	K24	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0206	K23	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0207	C24	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0208	F18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0209	F21	RESNONE	CLR_HIO5_DAT04	GPIO	HIGH						Internal PD 10K
WD0210	F20	RESNONE	CLR_VIO10R00	GPIO	HIGH						Internal PD 10K
WD0211	F20	RESNONE	BOOT_BLK_R0C0	GPIO	HIGH						Internal PD 10K
WD0212	A22	RESNONE	BOOT_BLK_W0E0	GPIO	HIGH						Internal PD 10K
WD0213	F18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0214	A20	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0215	J18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0216	M8	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0217	J18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0218	K21	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0219	K18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0220	J19	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0221	A18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0222	C18	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0223	J19	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0224	B17	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0225	A24	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0226	C17	RESNONE	NC	GPIO	HIGH						Internal PD 10K
WD0227	F7	RESNONE	SCLK1	GPIO	Tri-State						
WD0228	K7	RESNONE	SDATA1	GPIO	Tri-State						

Schematic Modify History

X01 --> AX1 Modify date 2012/2/24~2012/4/16
(2012/2/10 gerber out for EVT)

- 1.Change webcam port from port6 to port8.
- 2.Change RJ45 CONN P/N to 6026B0229101.
- 3. Add nc/ mark into part id of properties of the NC part.
- 4. Page 19 Add JHPD5 Jump P/N,記得MP後,拿掉包括header ,然後R308要上件,
- 5. Page 9 ADD JRTCCLR1-jump
- 6. Page 19 ADD JHPDB1-jump
- 7. Page 6,7 change C249,C265 to 1uf and add c317 C320 1 uf For vrefca 突波
- 8. Page 14 R278 249ohm change to 2.49k for Lan can't link issue.
- 9.page 8 remove edid component
- 10.page 19 co-lay mini pci 1.2 power solution
- 11.page 8 C1 10uf change to 47uf for falling time 下降太快
- 12.page 9 C123 8pf change to 10pf for RTC fail (page 1 RTC change list has been change)
- 13.page 10 remove R158,R213,R154,Q9,Q12,add R159 R176 for SCLK0 undershoot issue.
- 14.(power modify)page 22 change PR25 20k to 1k and add PC157 & PC77 for +1.2v waveform
- 15.page 10 bios request add R150 to 0ohm R143 & R146 to 10k
- 16.page 16 add 2 cap for EMI solution
- 17.page 19 add JPHDB3 for bios request
- 18.page 14 R12 R8 from 220ohm to 33ohm for LED over dark problem

AX1 -->A01 Modify date 2012/4/16 ~ 2012/5/22
(2012/5/22 gerber out for PVT)

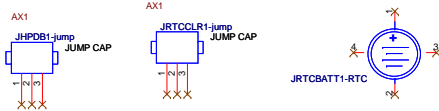
- 1.page 20 PC60 to 1uf,PR59 to 1k for +5v_s0 drop issue
- 2.page 14 c105 c107 change to 12pf for vendor suggestion
- 3.page 21 del PU11,PC148,PC152,PC155,PC156,PR182,PR185,PR186,PC147 I-LIMIT function disable
- 4.page 24 PR4,PR28,PR104,PR114,PR115,PR116,PR124,PR126 0 ohm change to default short
- 5.page 9 change R184 from R0402_short to R0402 footprint
- 6.page 1 NPI request increase optical point
- 7.page 26 change hole footprint to NP from H5 to H8

A01 -->A02 Modify date 2012/6/21 ~ 2012/7/11
(2012/7/16 gerber out for MP)

- 1.page 19 removed Debug circuit R310,JLPCDB1,L1,L2,L3,C14,C15,C16,R28,R29,R30,U1,U2,R10,R11,Q1,Q2,C3,C4,R1,R2,D23,JCRTDB1,D22,R53,R54,D9,D10,D11,D12.D13,D14,U31,Q22,R312,R313,R314,R315,R316,R317,Q22,C21,C22,C7,R25,R26
- 2.page 19 because remove jump cap so ADD R308 to short SPI CS#
- 3.page 22 because anx3110 timing sequence removed PR25,add PR1 to 1k
- 4.page 16 web cam display issue. AMD recommand remove R532 & C390
- 5.page 14 vendor suggest removed R246 & R227. because Lan IC has been build circuit inside.

PVT & MP de-pop circuit

- page 18 for EC debug connector
JECDB1
- page 19 for HP debug
JLPCDB1,R310,JHPDB5,C147,U31,Q22,D9~D14,R312~R317
- page 19 for AMD debug
U5,R53,R56,R77,R80
- page 19 for TnI HW debug (CRT output function)
C3,C4,C7,C14,C15,C16,C21,C22,D22,D23,JCRTDB1
L1,L2,L3,Q1,Q2,R1,R2,R3,R4,R5,R6,R10,R11,R14
R15,R25,R26,R28,R29,R30,R32,R36,R37,U1,U2



TPV (Top Victory Electronics Co., Ltd.)	OEM MODEL	HP_Arroyo3	Size	C
據路圖編號 1	TPV MODEL	Arroyo3	Rev	A02
Key Component	TABLE OF CONTENTS	PCB NAME	6050A2516401	備註 <備註>
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